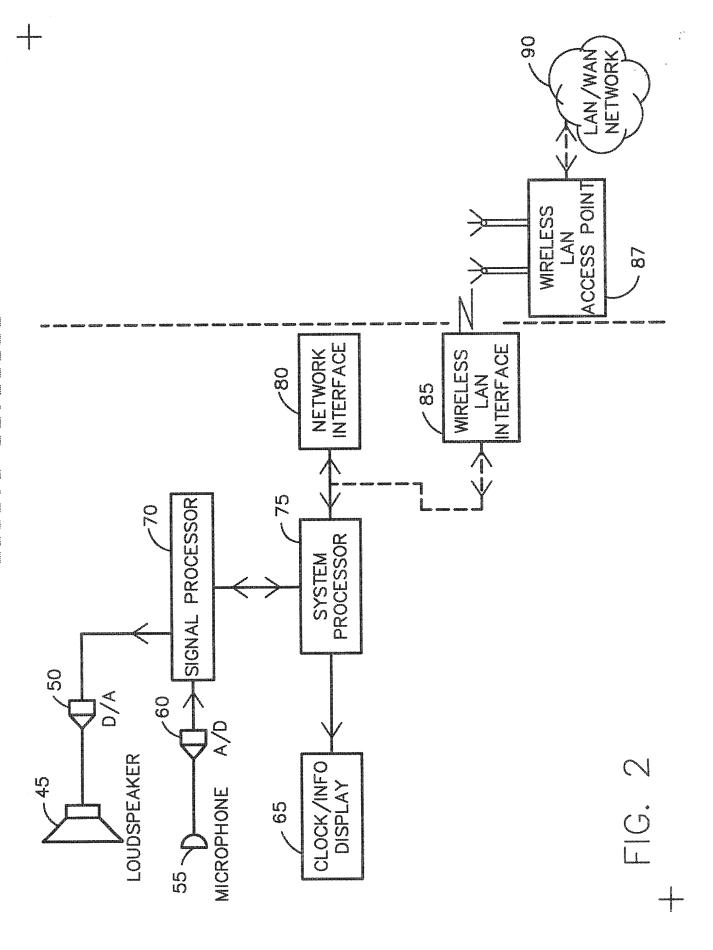
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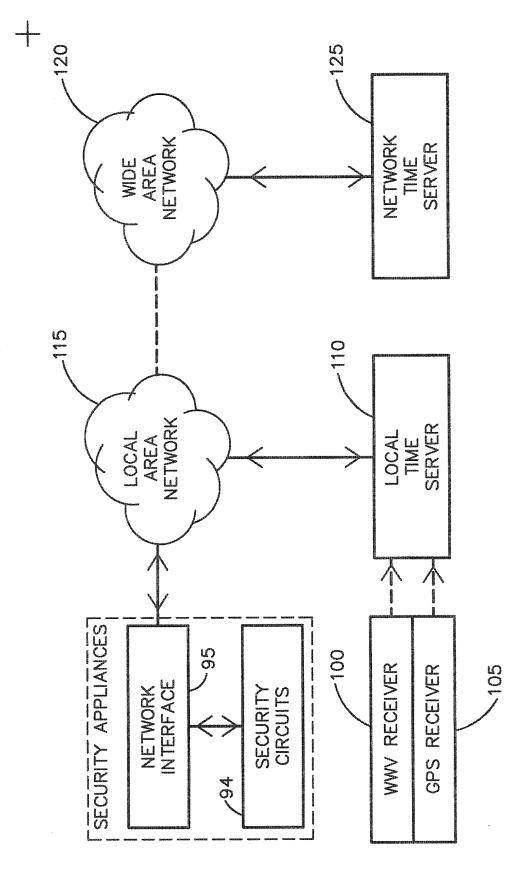
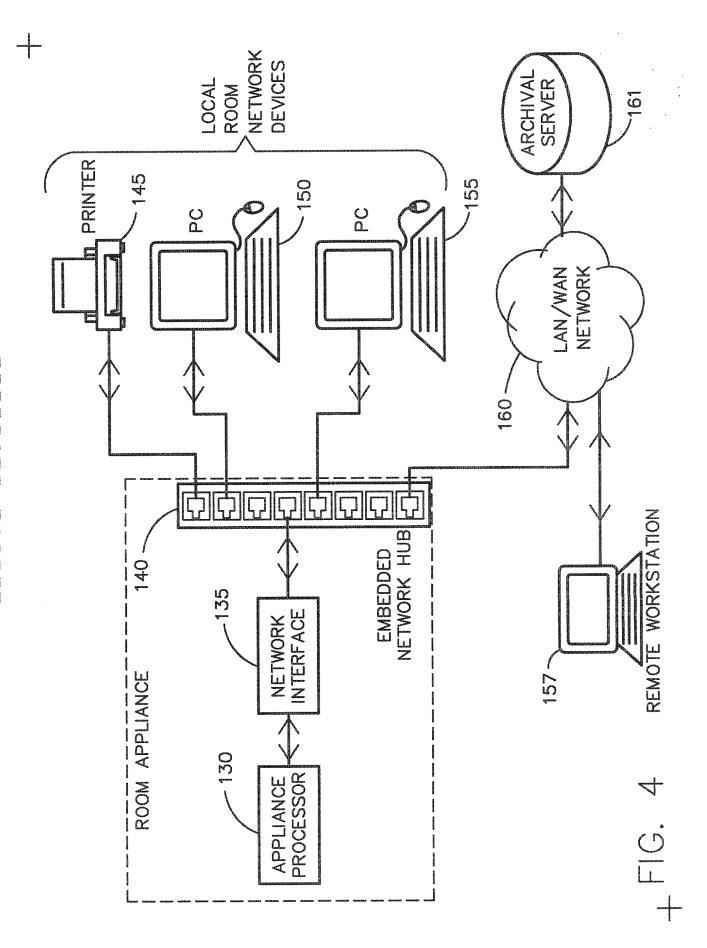
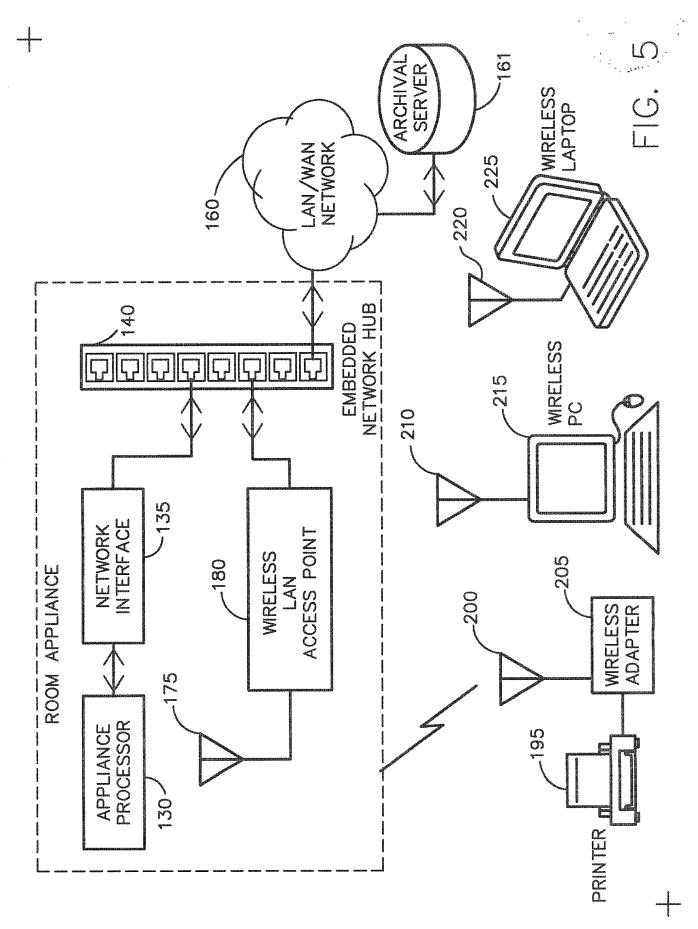
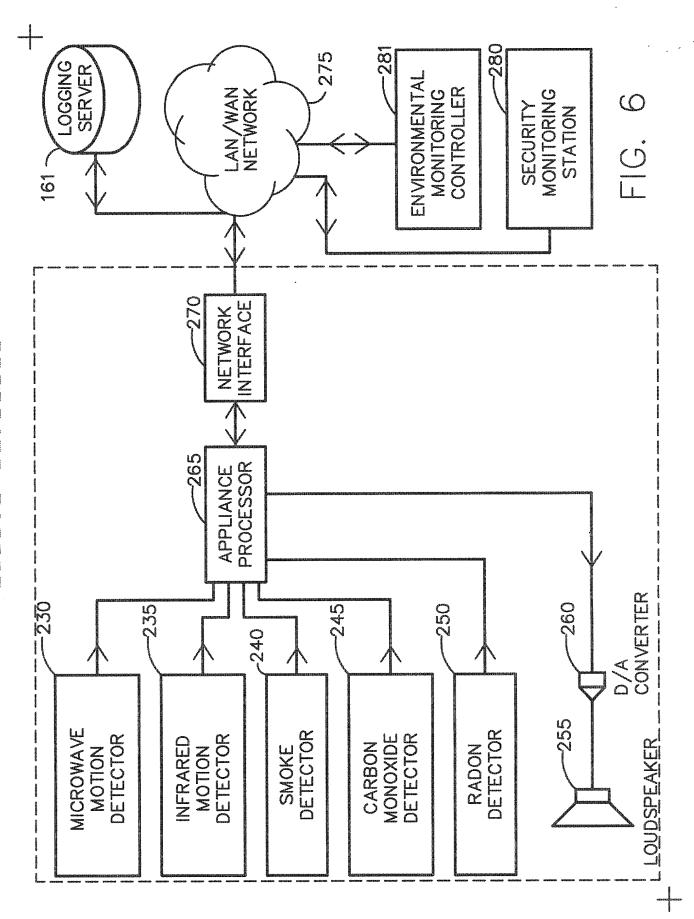


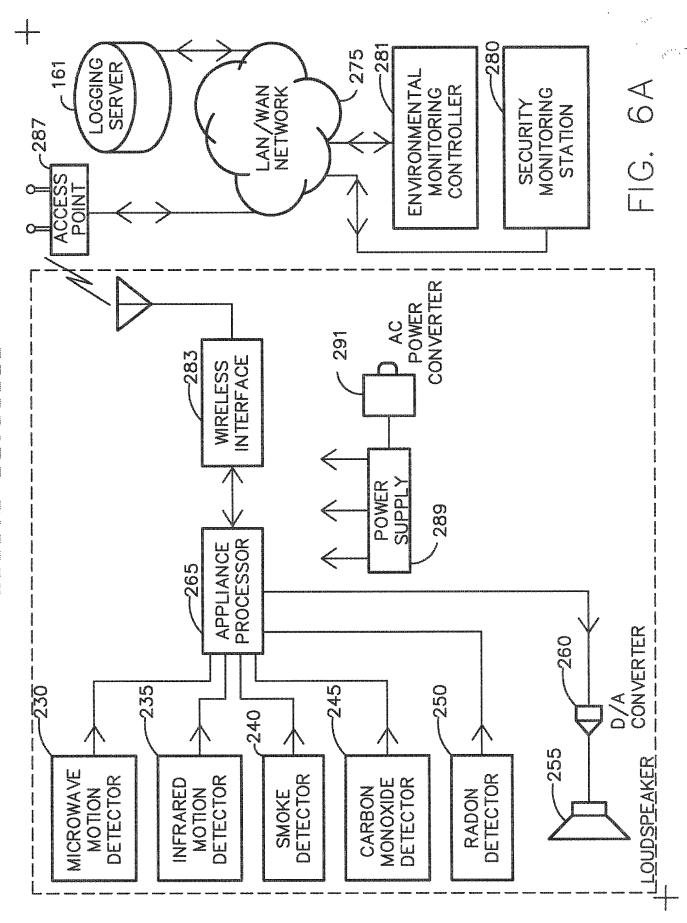
FIG. 3

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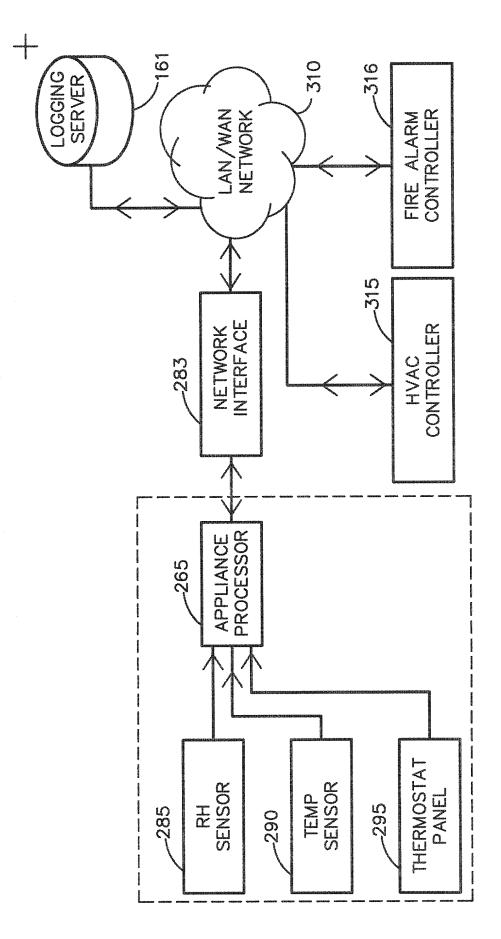
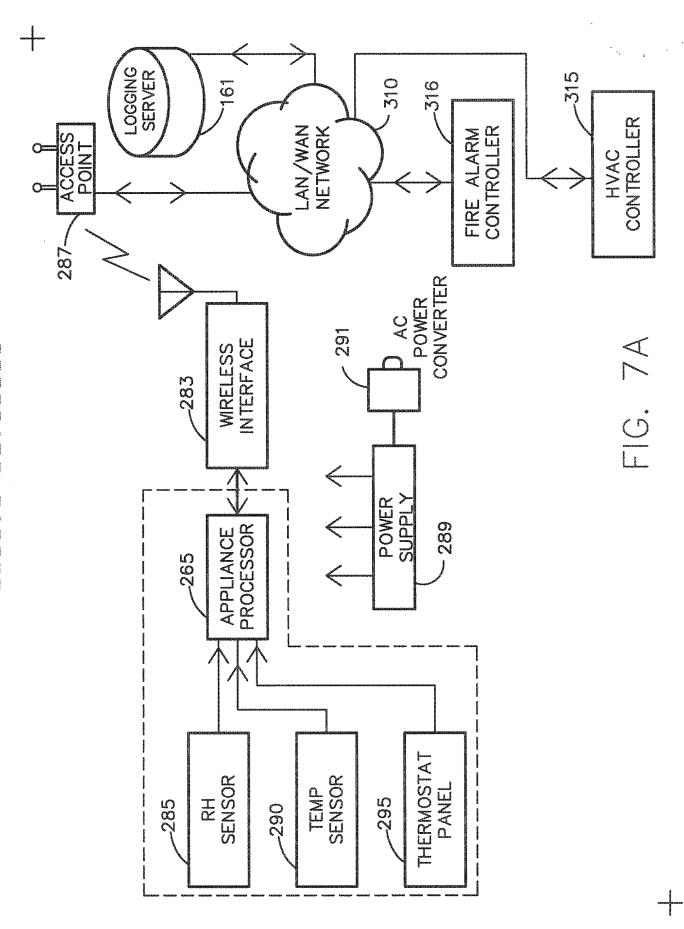
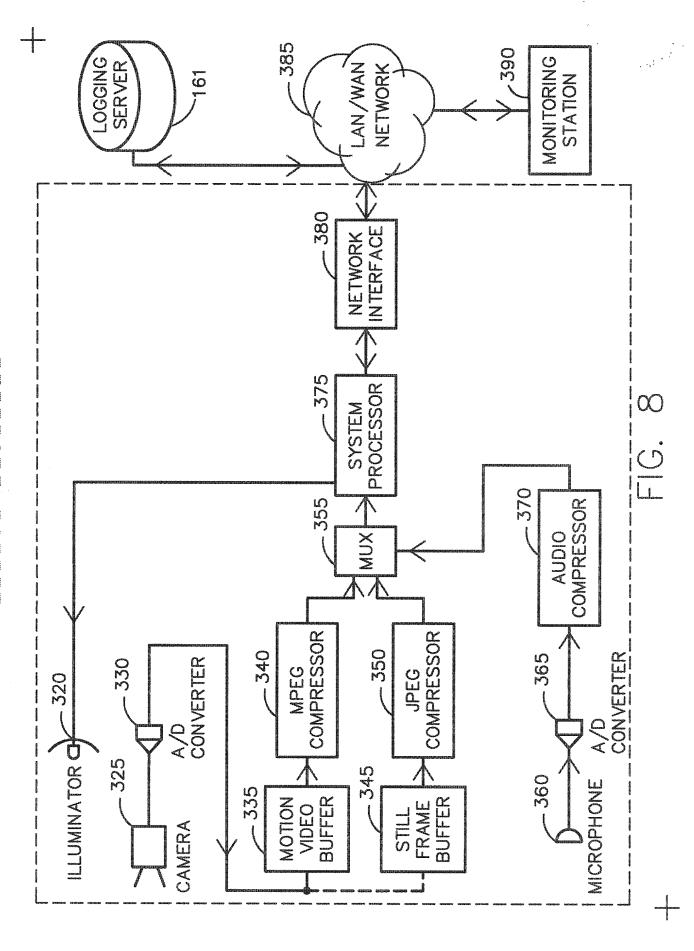
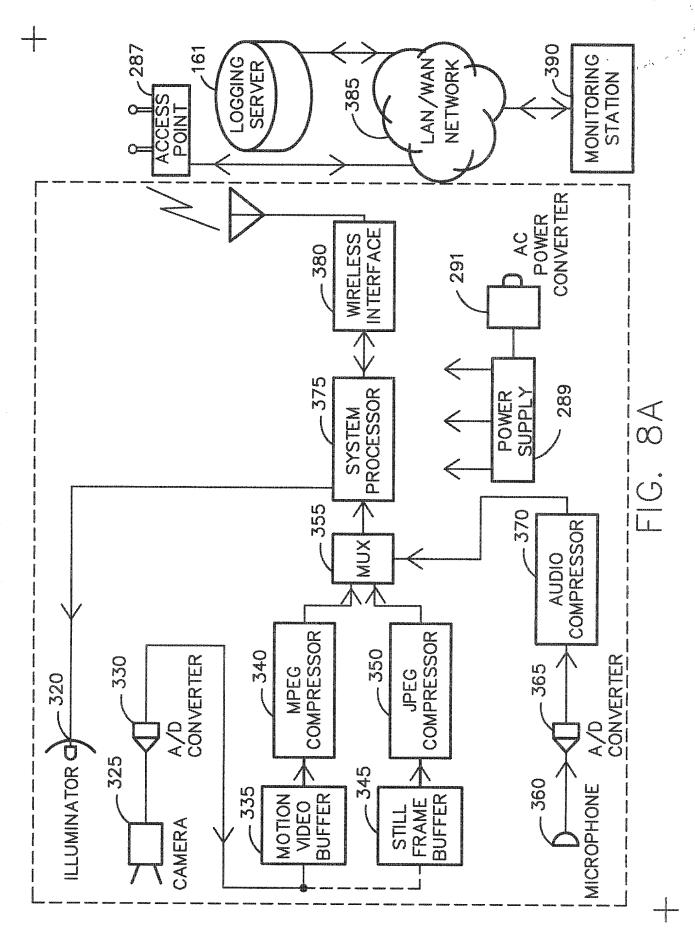


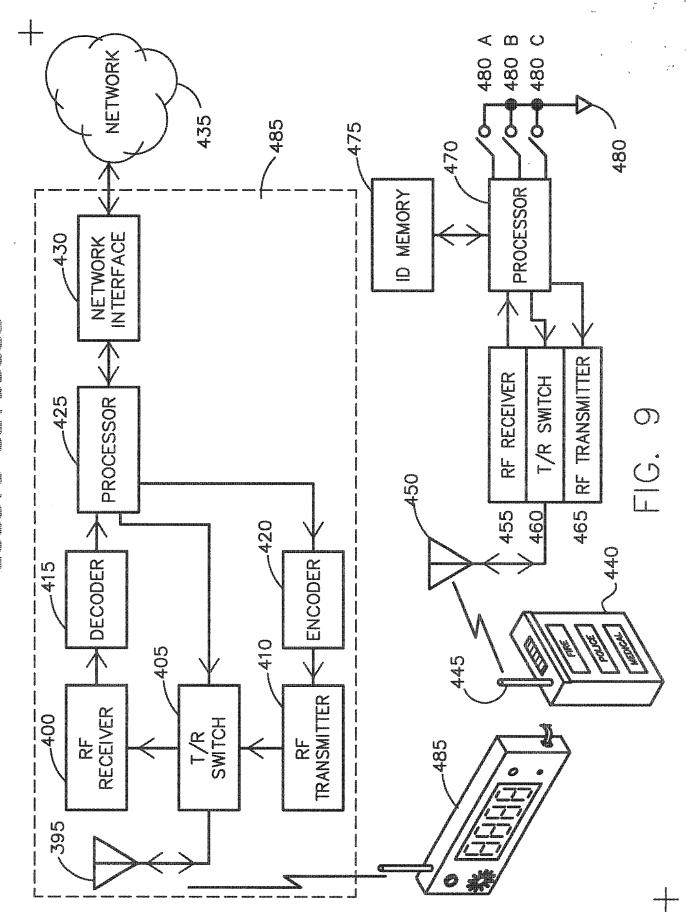
FIG. 7

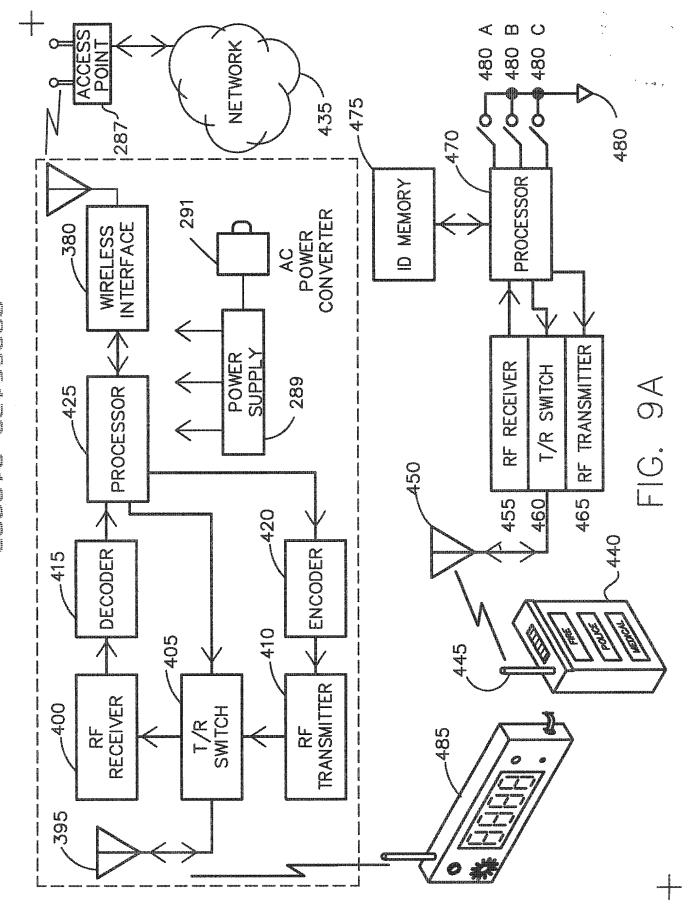




ĭ







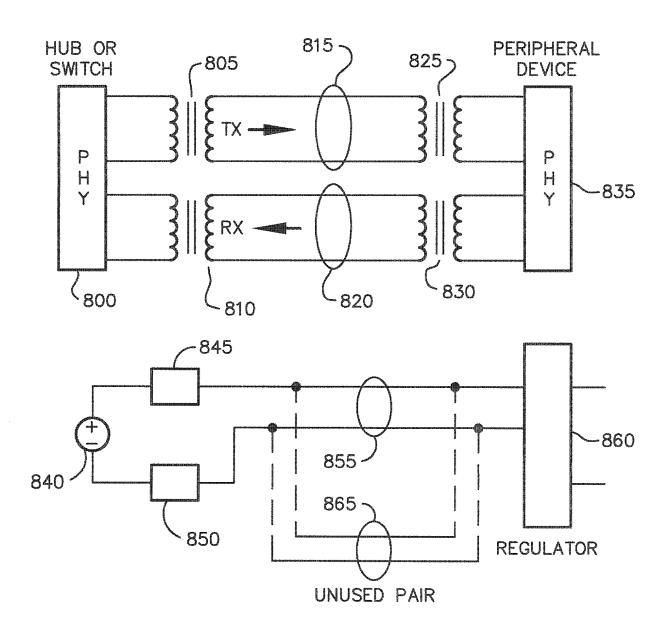


FIG. 10

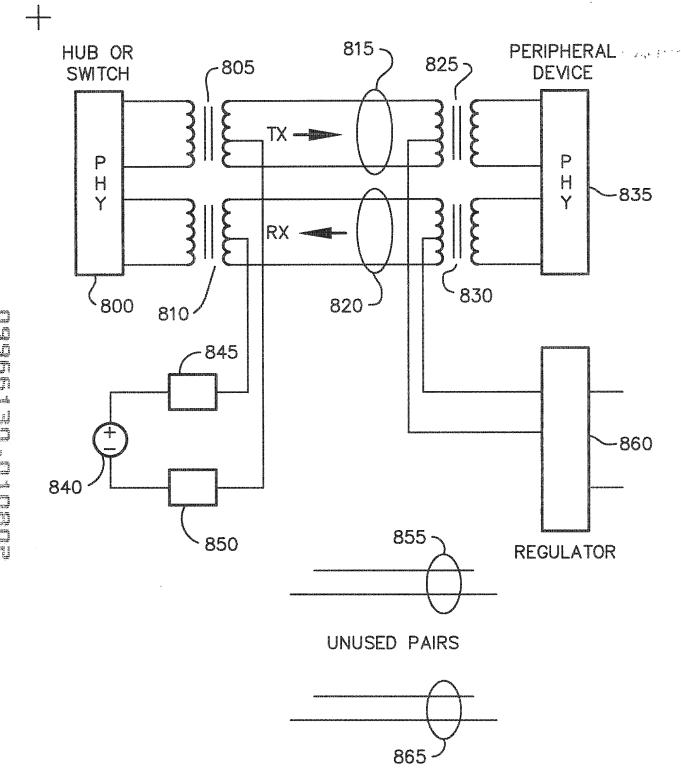
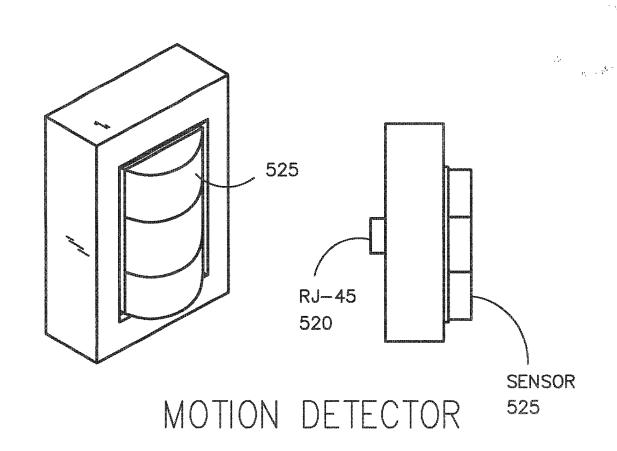


FIG. 11



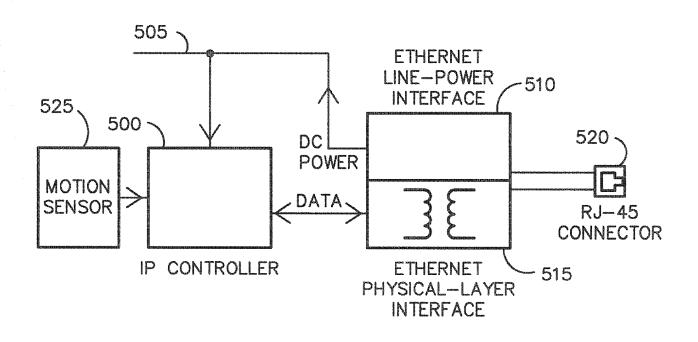


FIG. 12

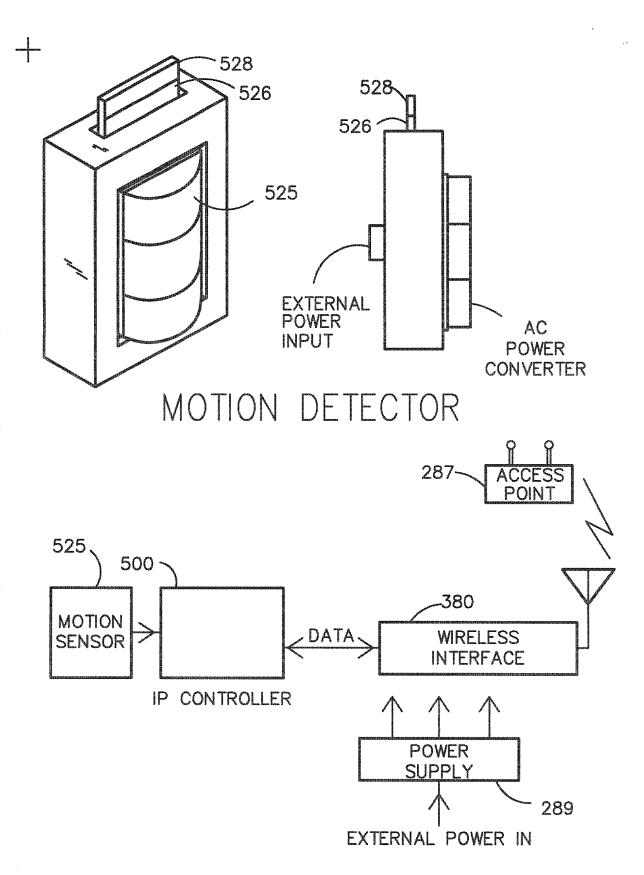
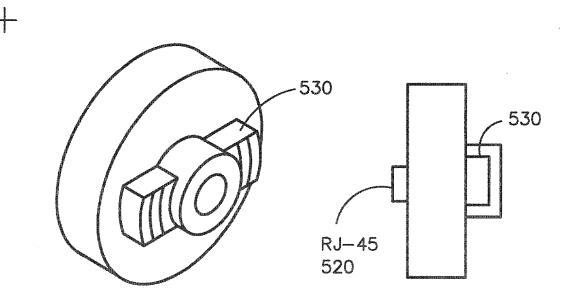


FIG. 12A



# SMOKE DETECTOR OR SMOKE & TEMP DETECTOR

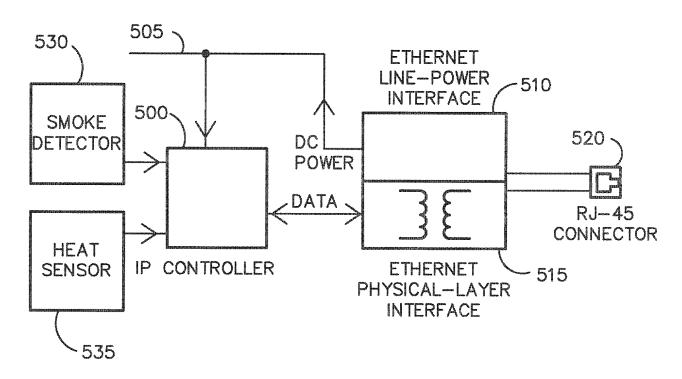
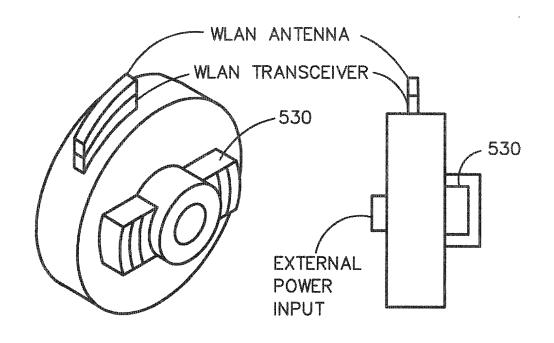
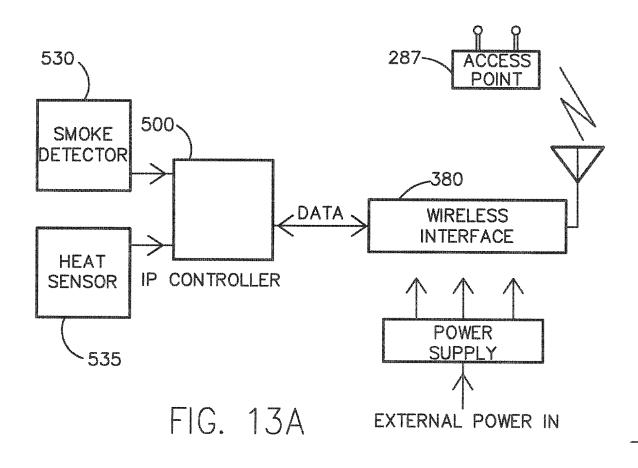
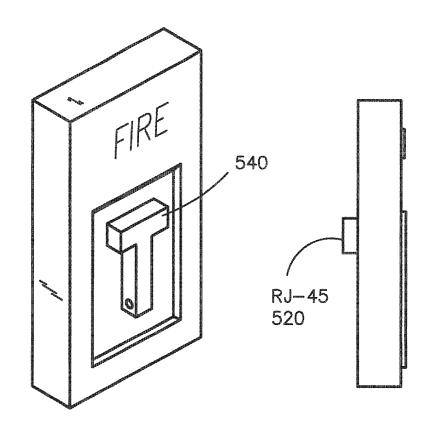


FIG. 13

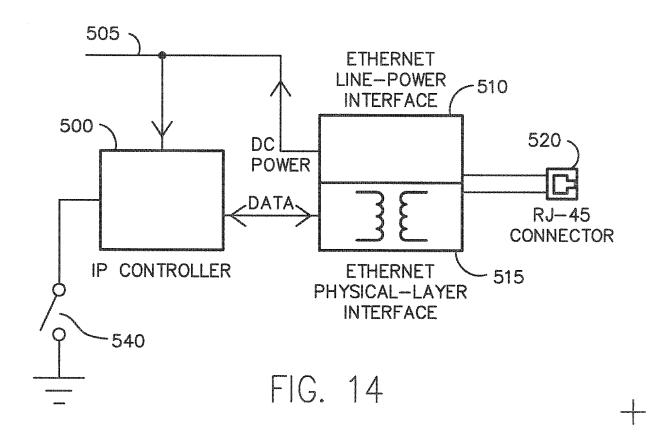


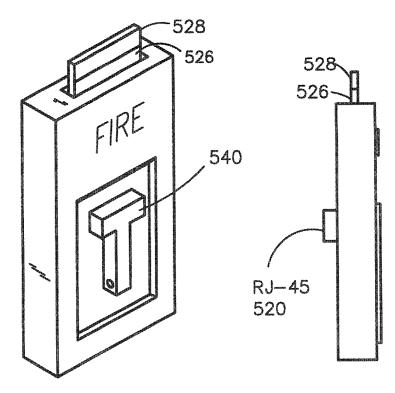
SMOKE DETECTOR OR SMOKE & TEMP DETECTOR



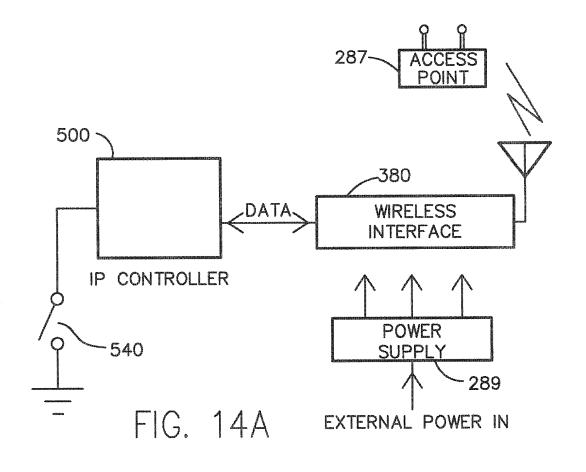


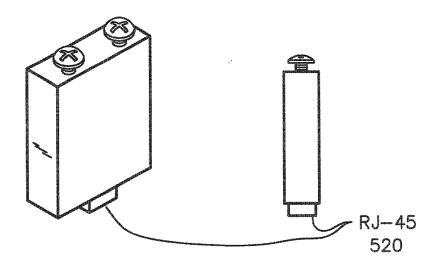
PULL HANDLE



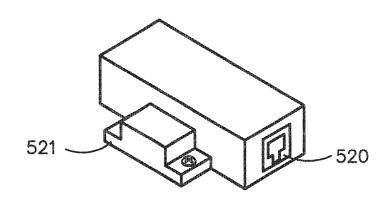


PULL HANDLE

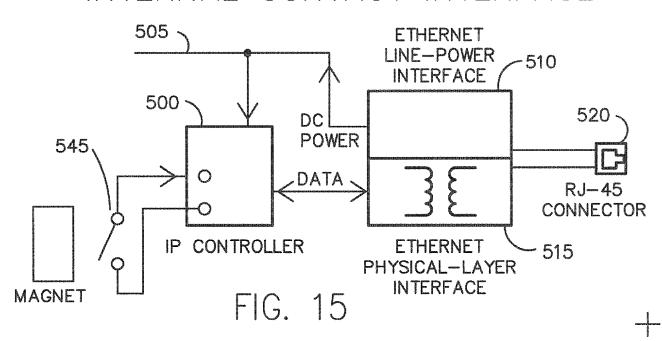


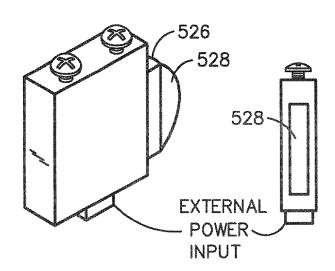


#### EXTERNAL CONTACT INTERFACE

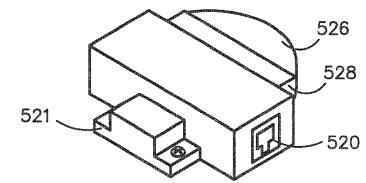


## INTERNAL CONTACT INTERFACE

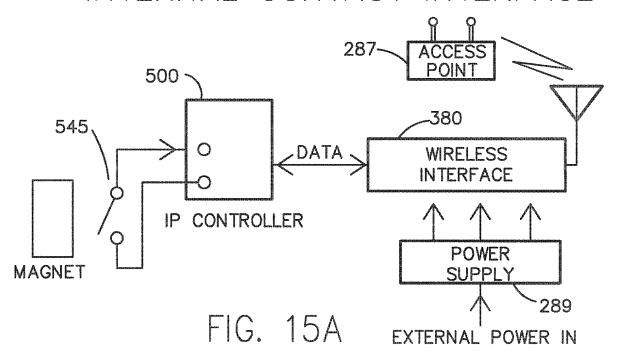


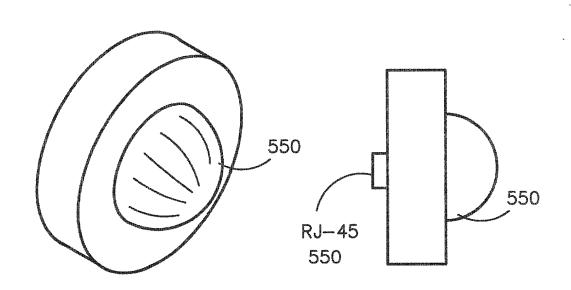


#### EXTERNAL CONTACT INTERFACE



## INTERNAL CONTACT INTERFACE





HEAT SENSOR

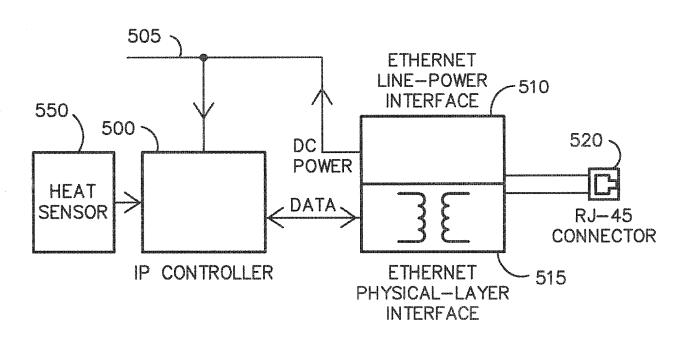
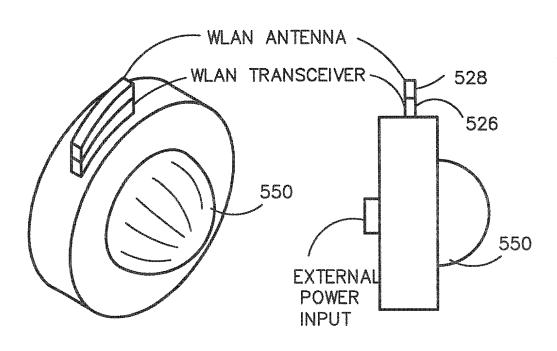


FIG. 16



HEAT SENSOR

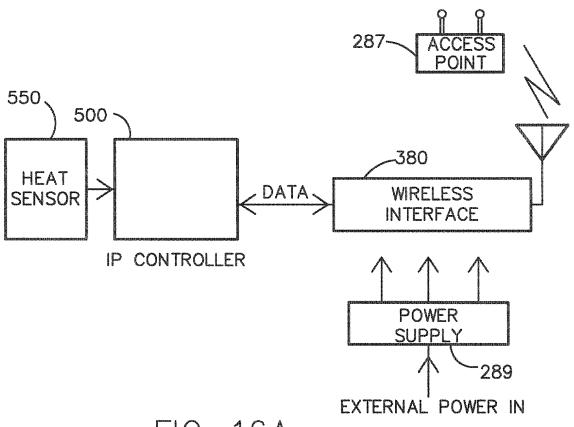
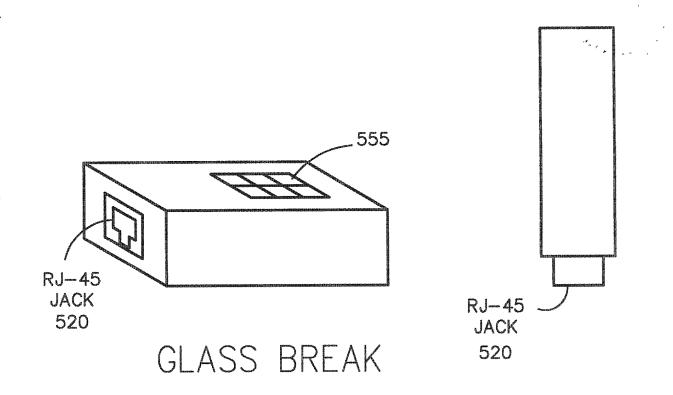


FIG. 16A

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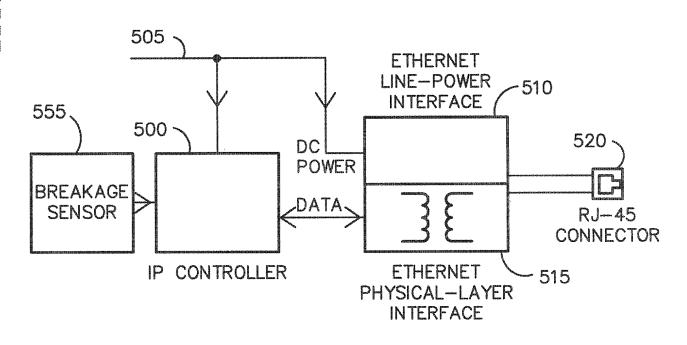
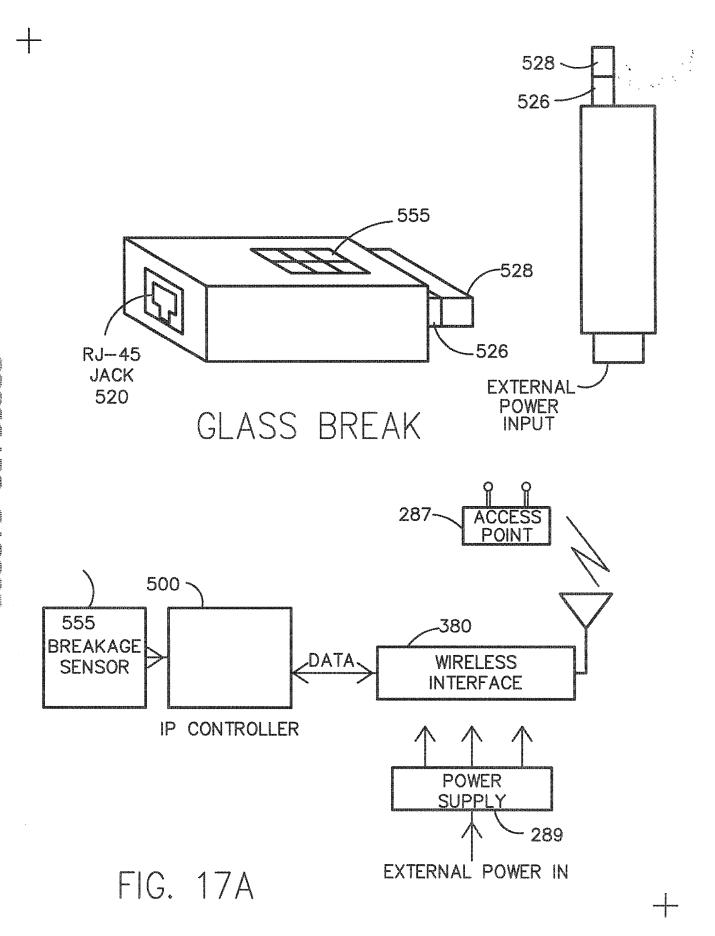
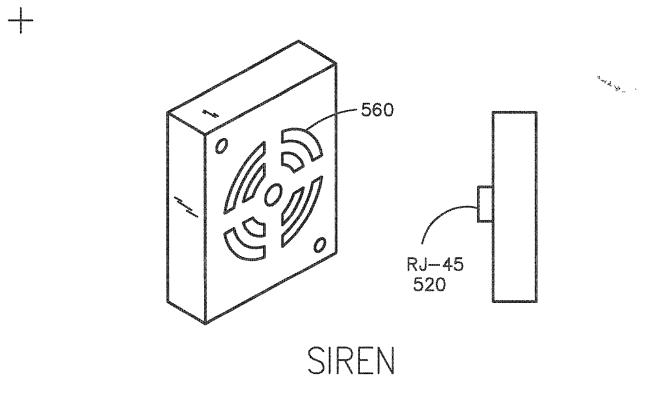


FIG. 17





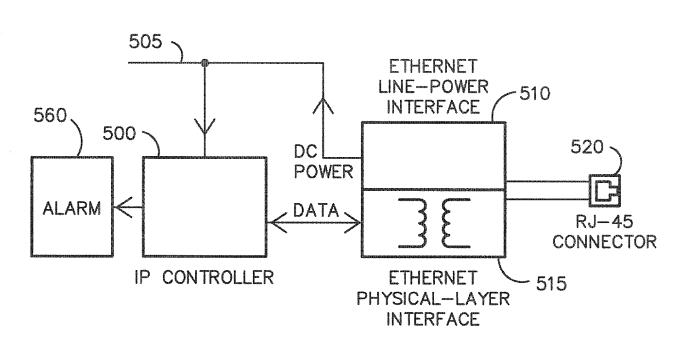
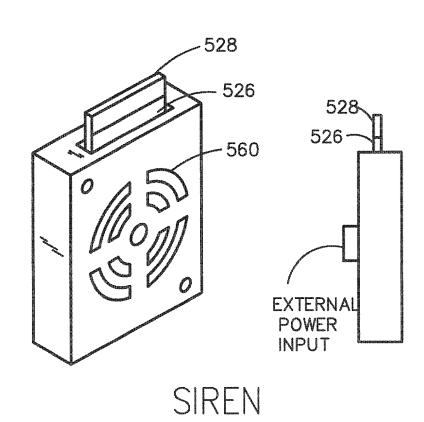
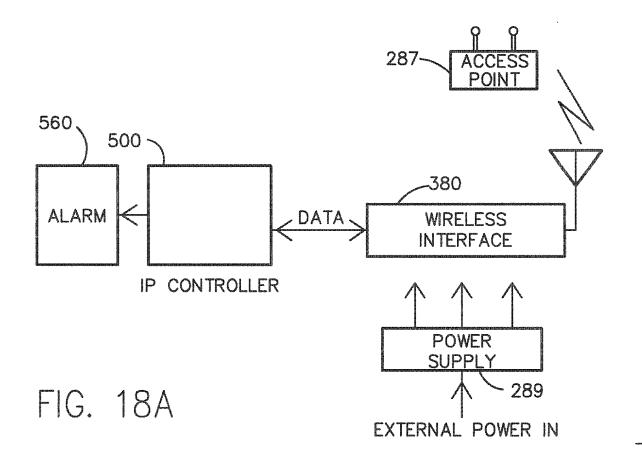


FIG. 18





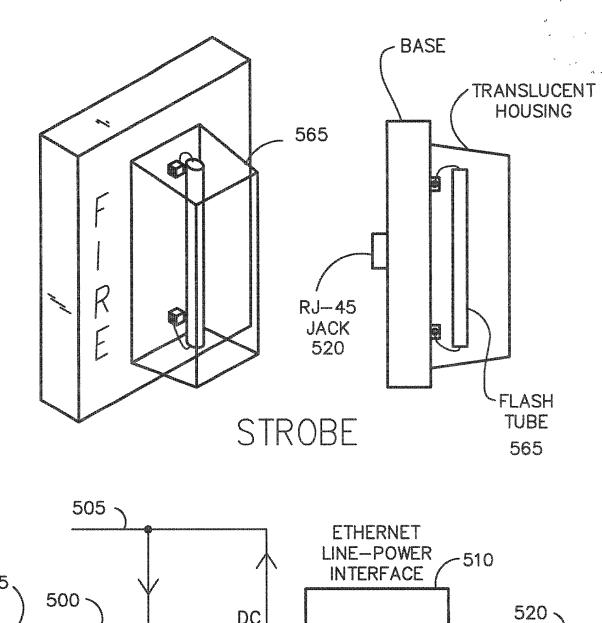
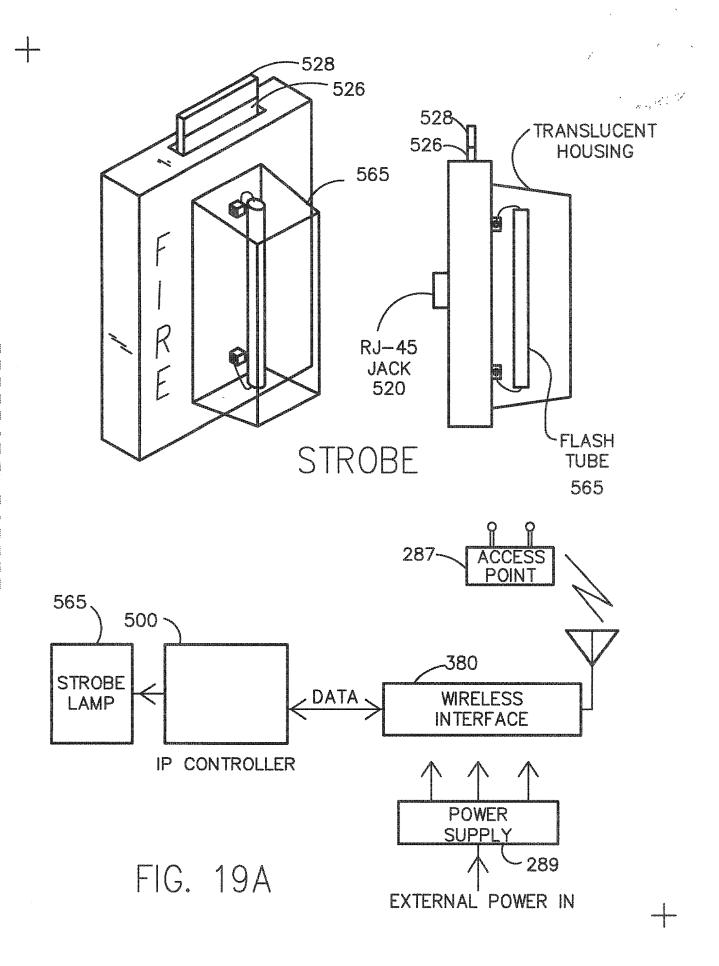
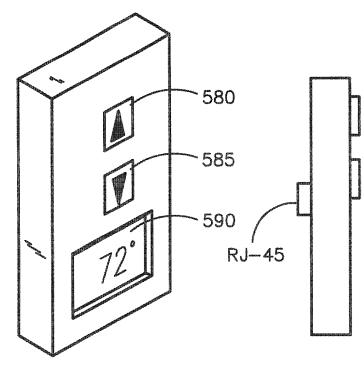
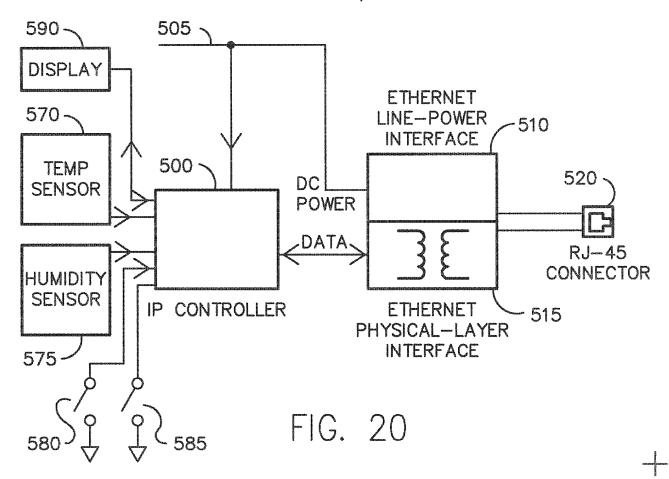


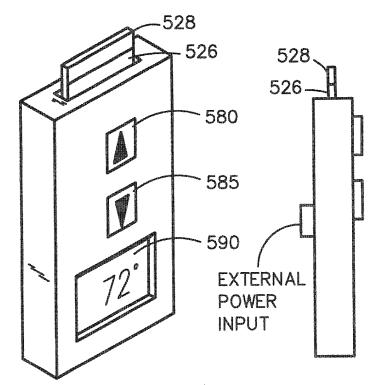
FIG. 19



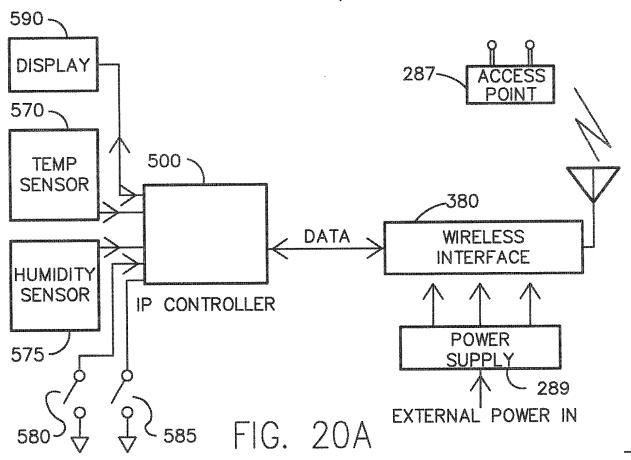


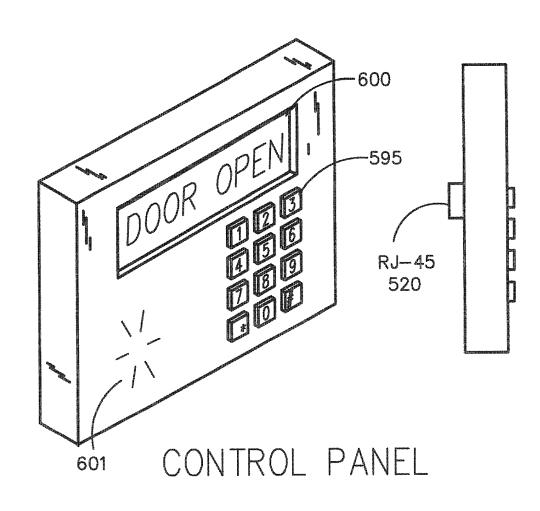
THERMOSTAT AND/OR HUMIDISTAT

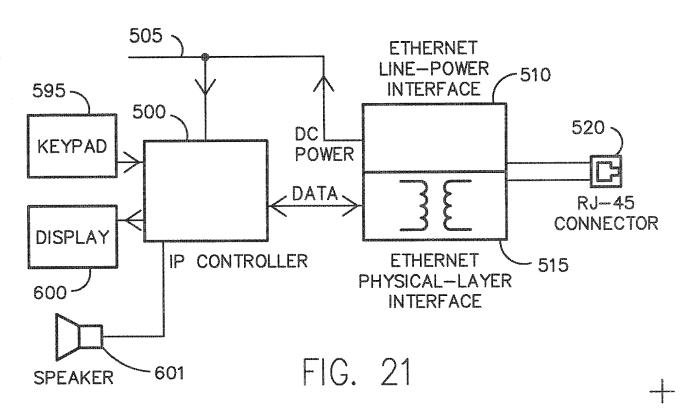


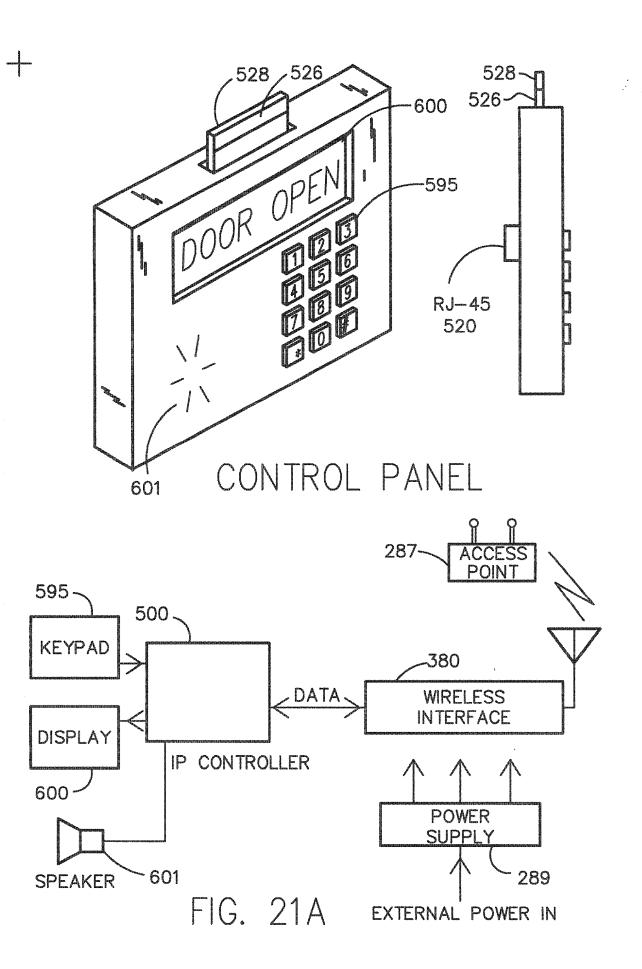


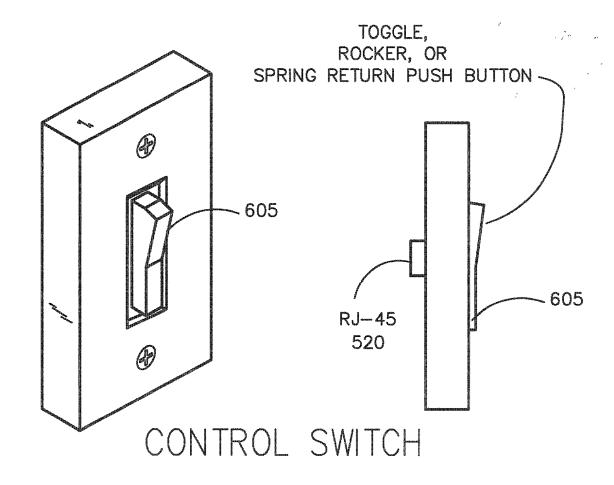
THERMOSTAT AND/OR HUMIDISTAT

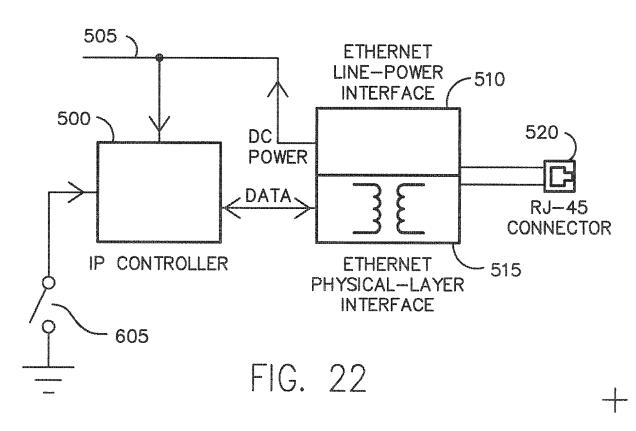


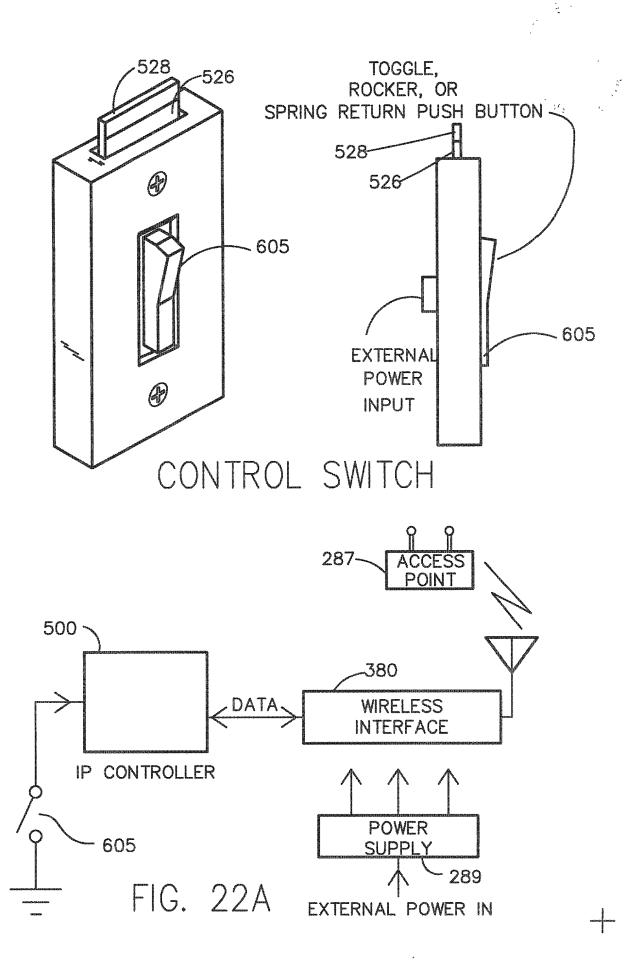


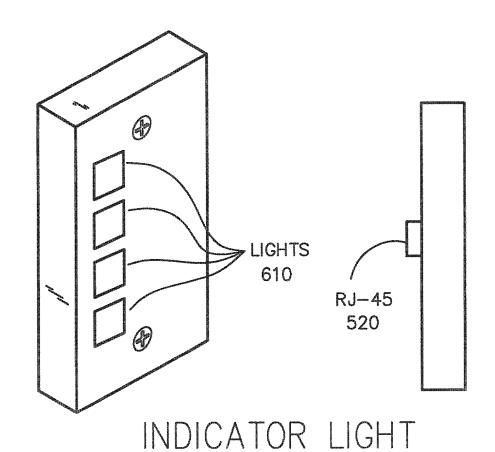


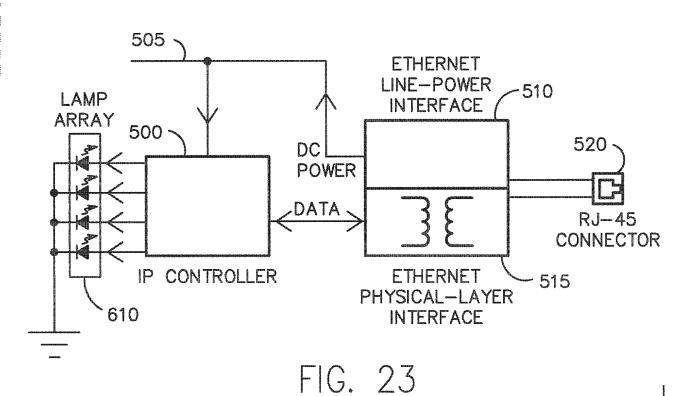


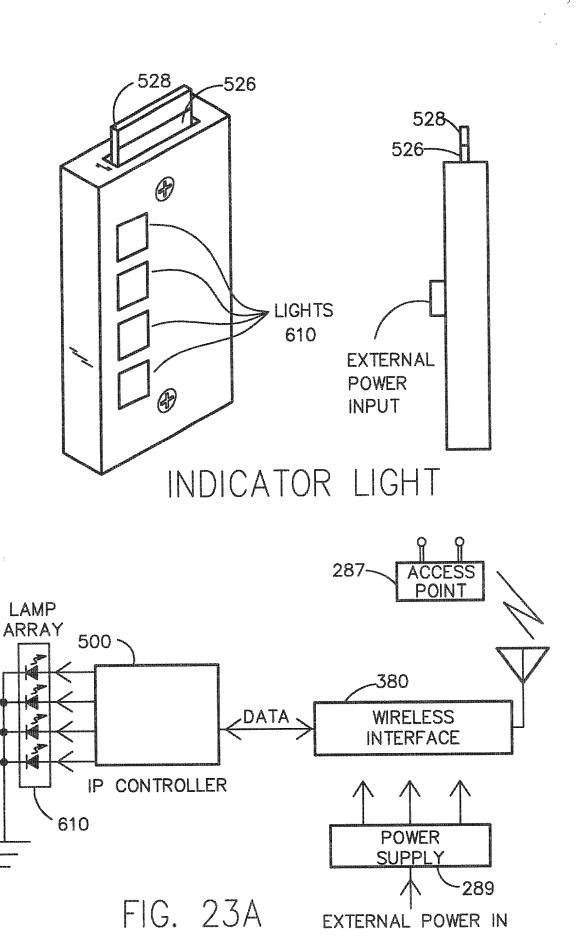


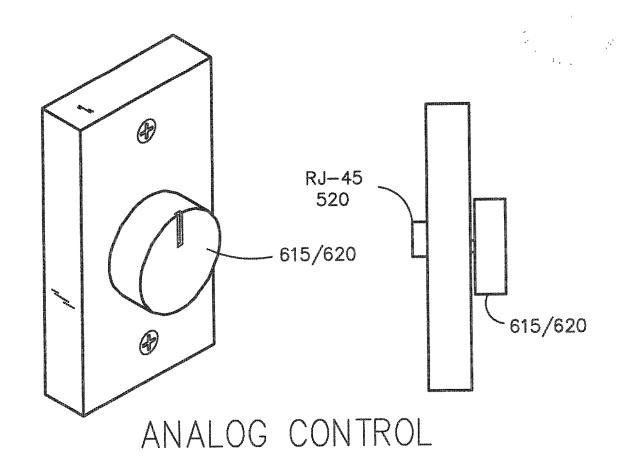


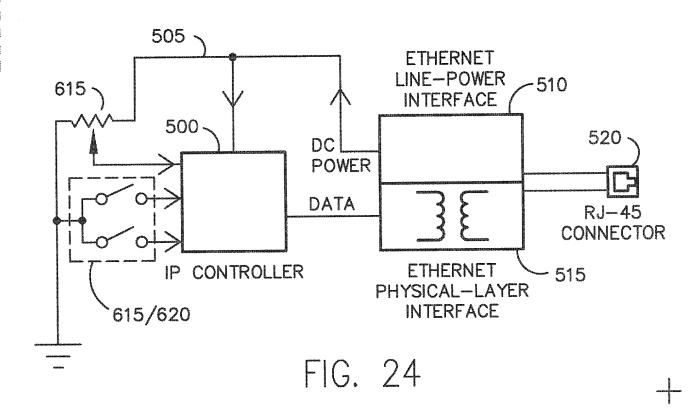


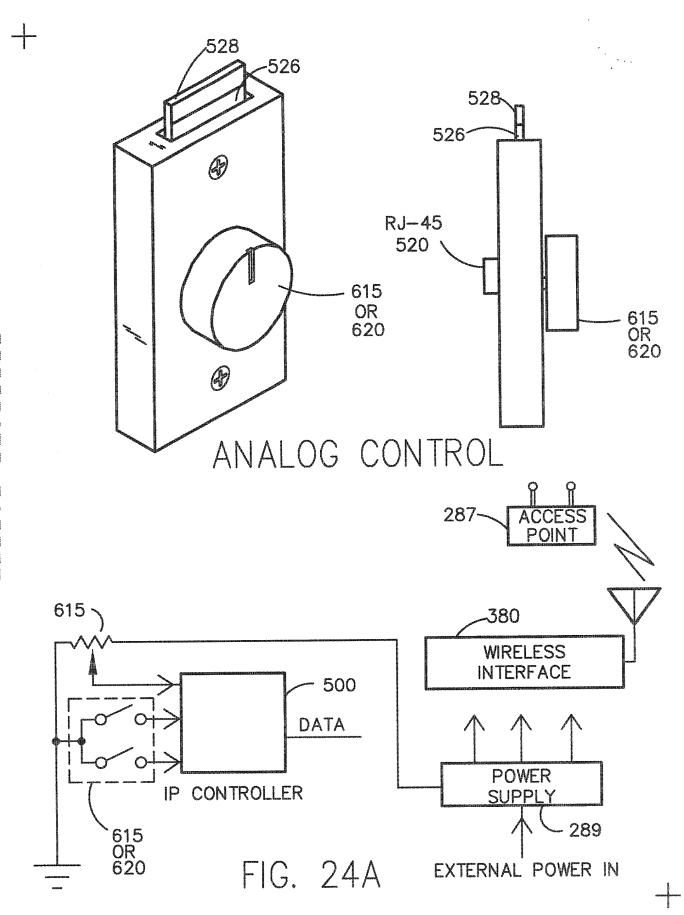


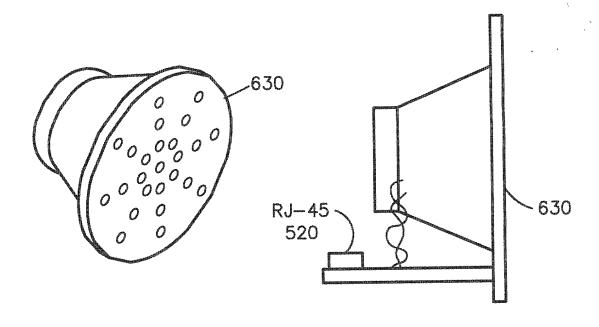












LOUDSPEAKER WITH AMPLIFIER

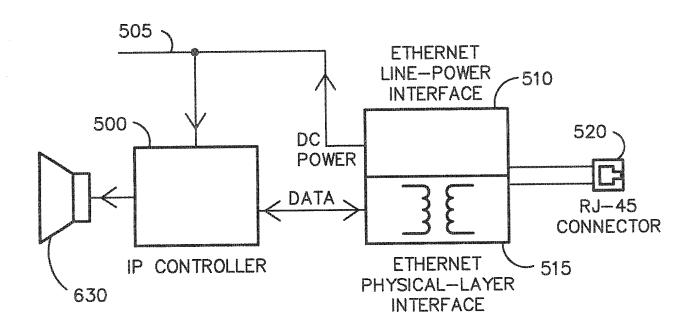
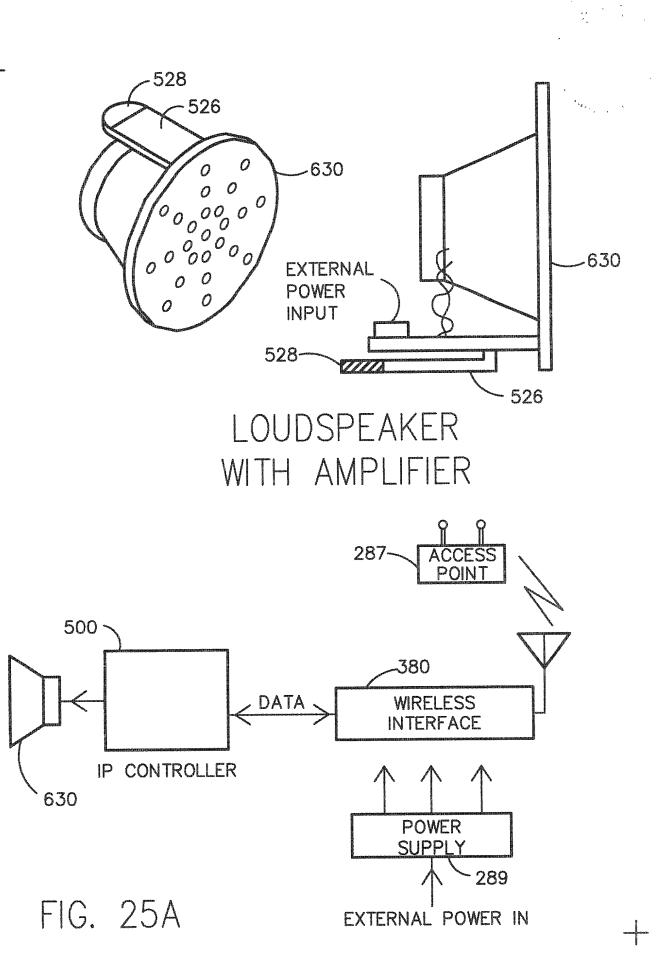
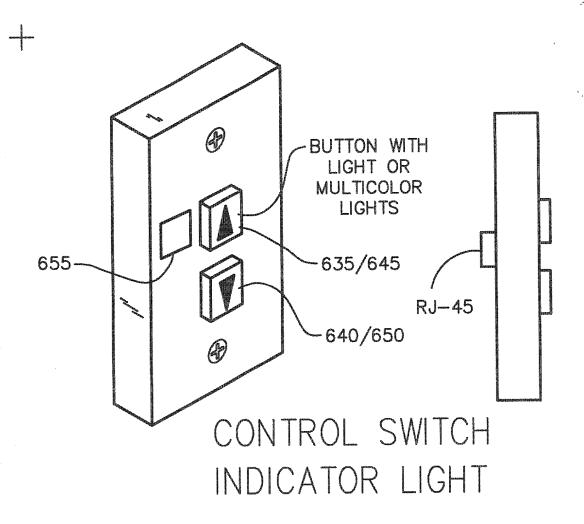
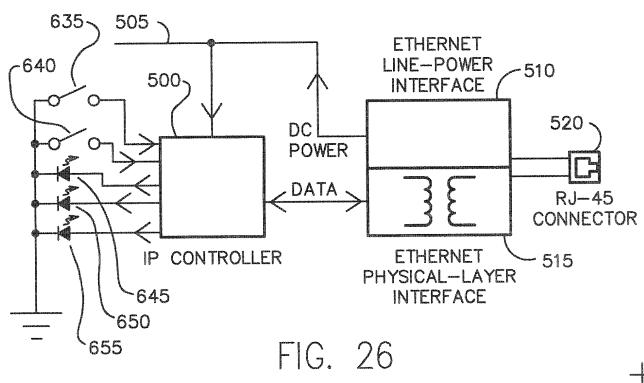
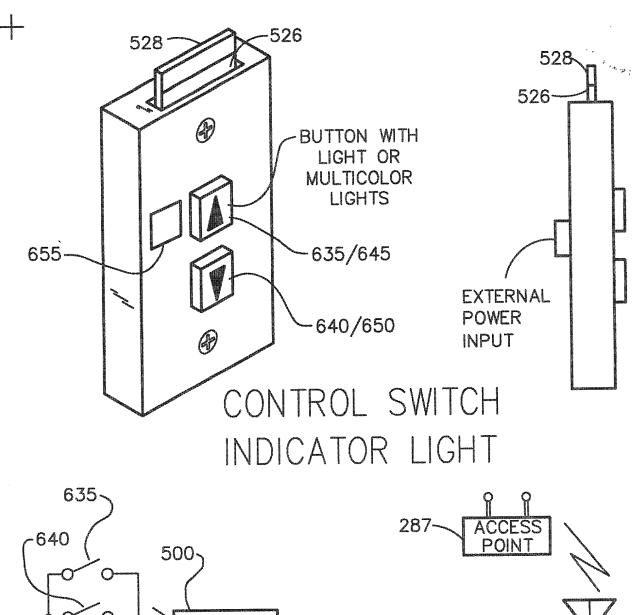


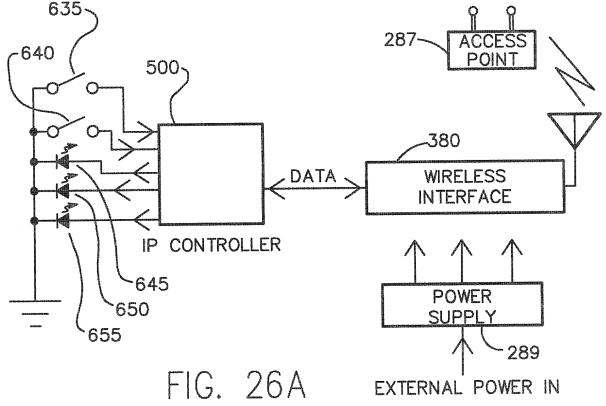
FIG. 25

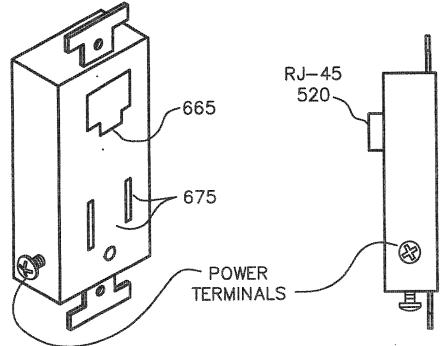




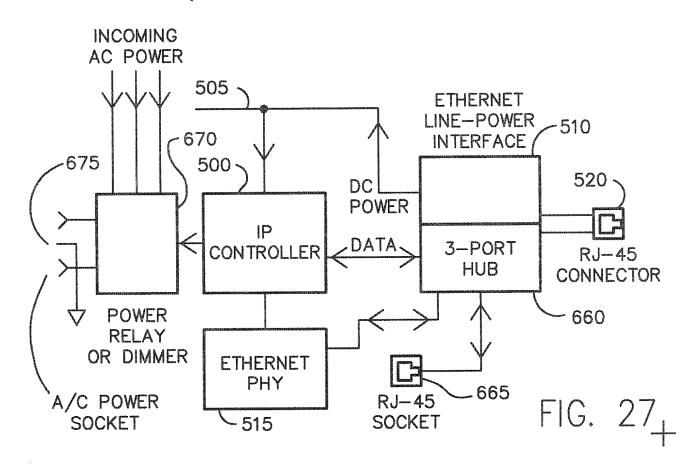


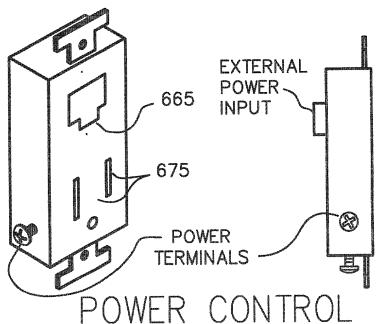






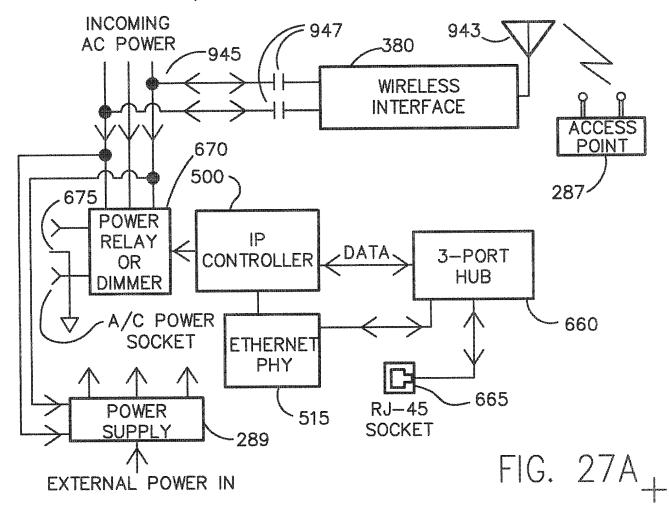
POWER CONTROL (SWITCH OR DIMMER)

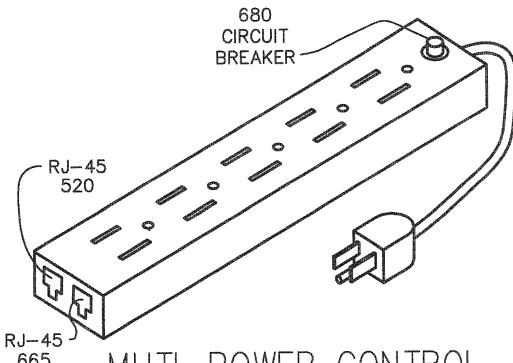




the first of

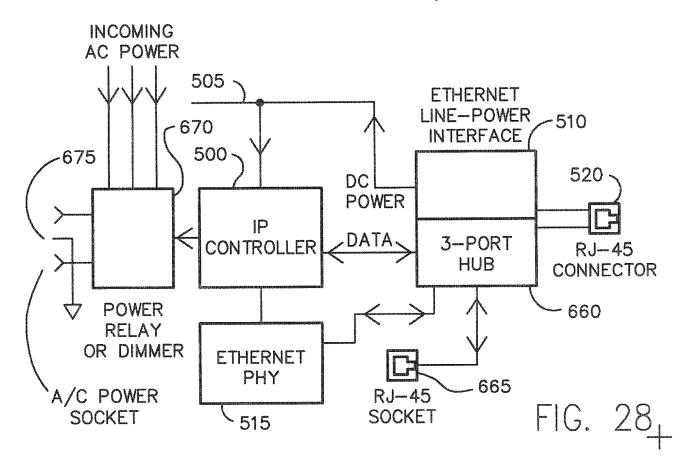
(SWITCH OR DIMMER)

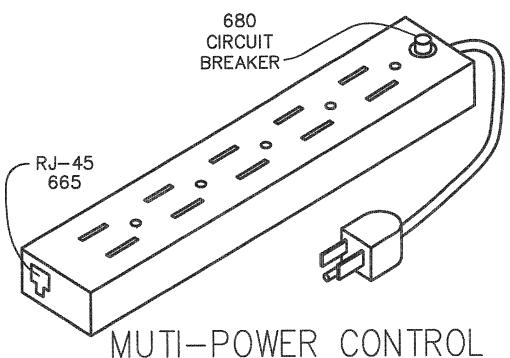




MUTI-POWER CONTROL

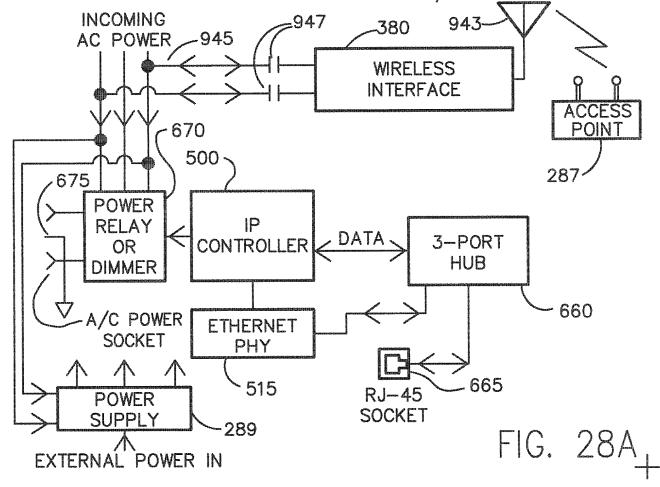
MULTIPLE SWITCH AND/OR DIMMER

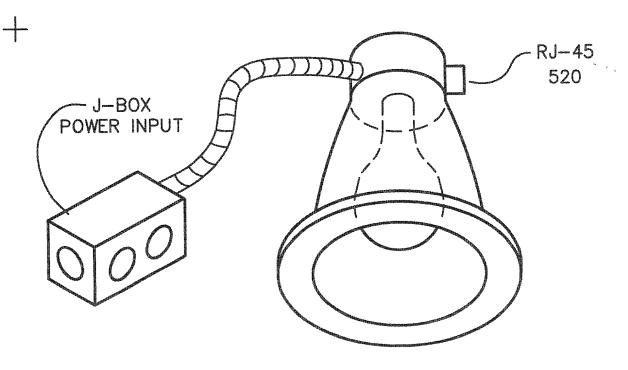




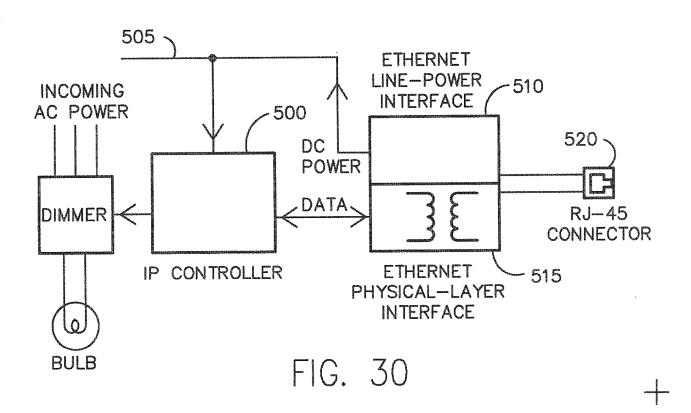
MUTI-POWER CONTROL

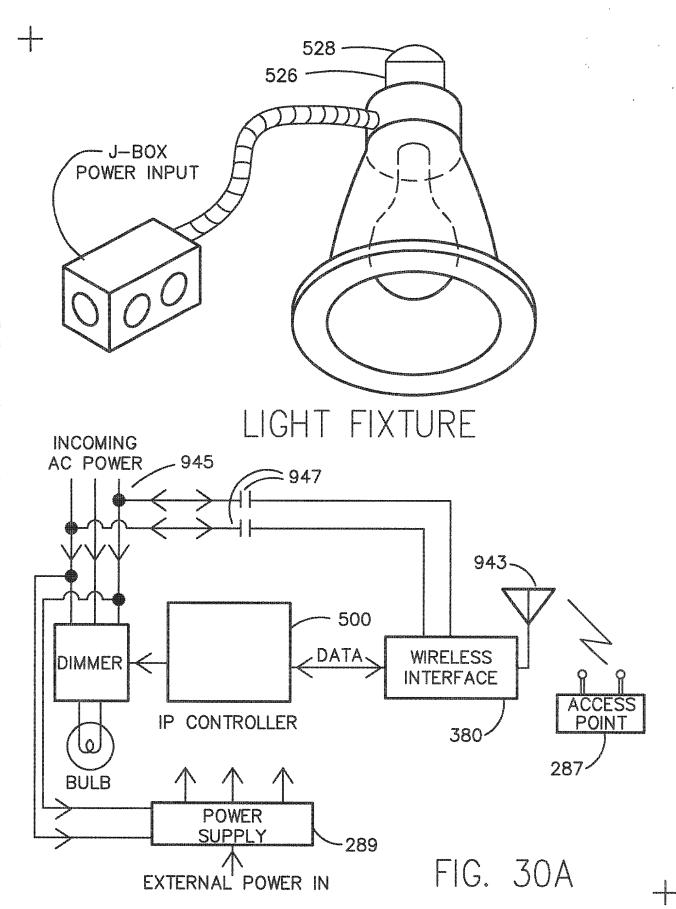
MULTIPLE SWITCH AND/OR DIMMER

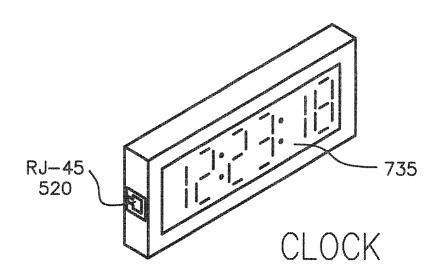




LIGHT FIXTURE







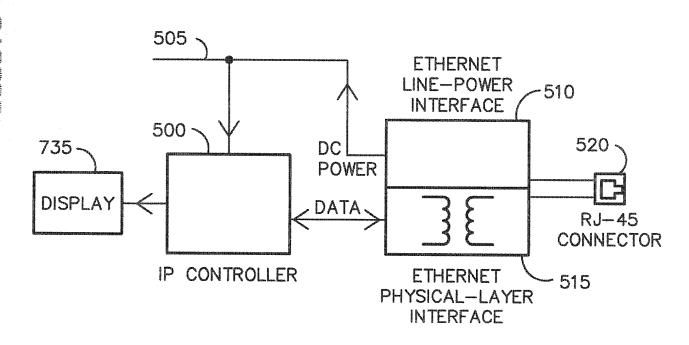
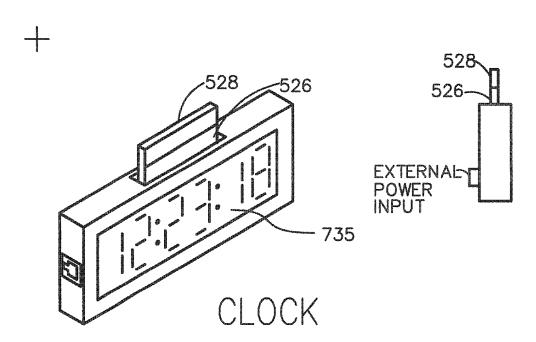


FIG. 32



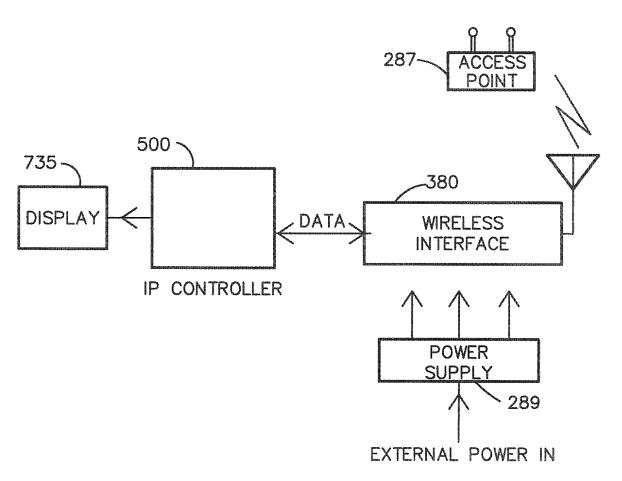
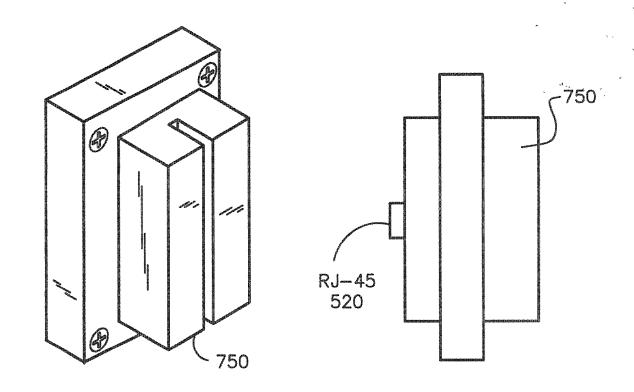


FIG. 32A

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MAG STRIP READER

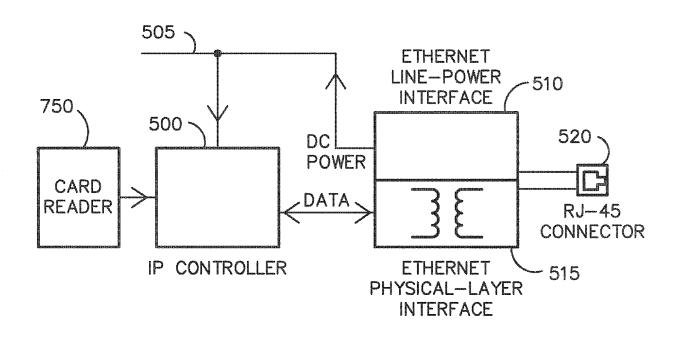
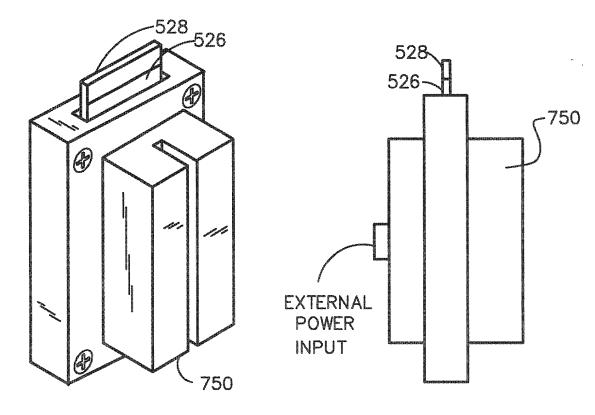
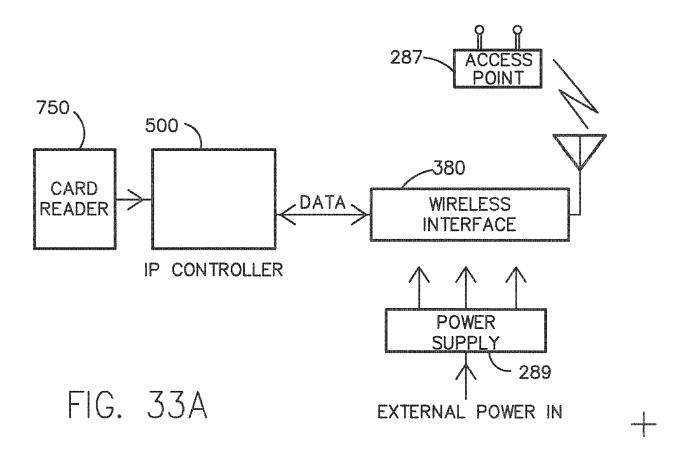
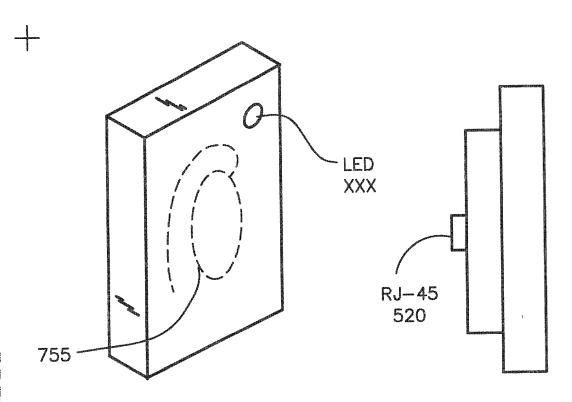


FIG. 33



MAG STRIP READER





PROXIMITY CARD READER

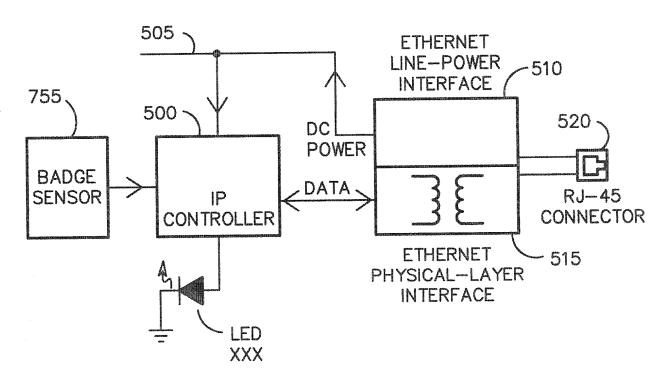
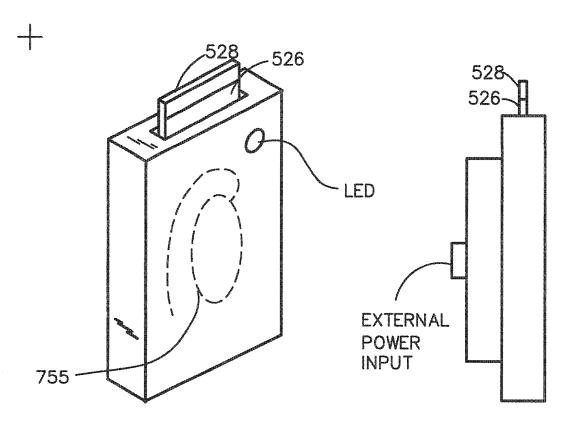
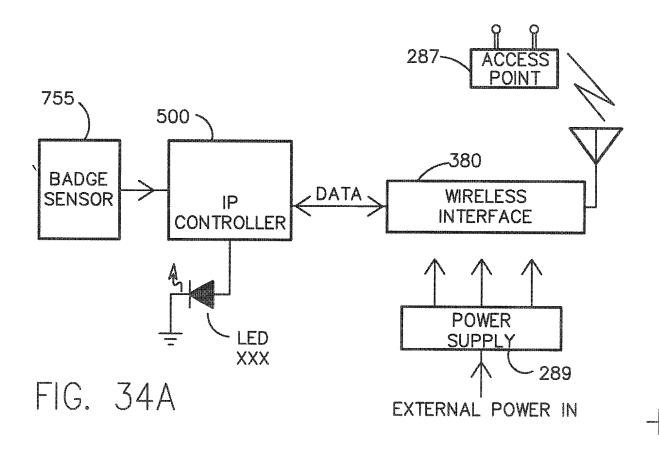
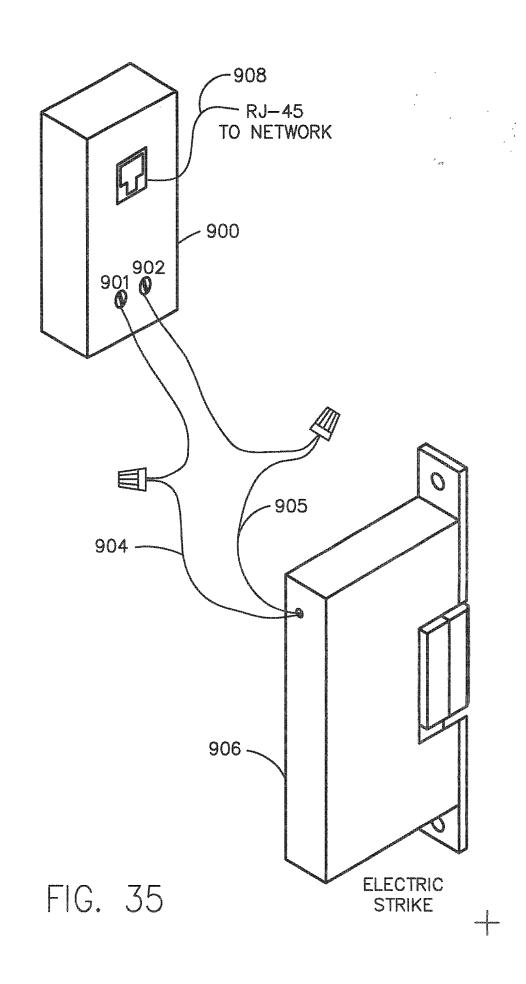


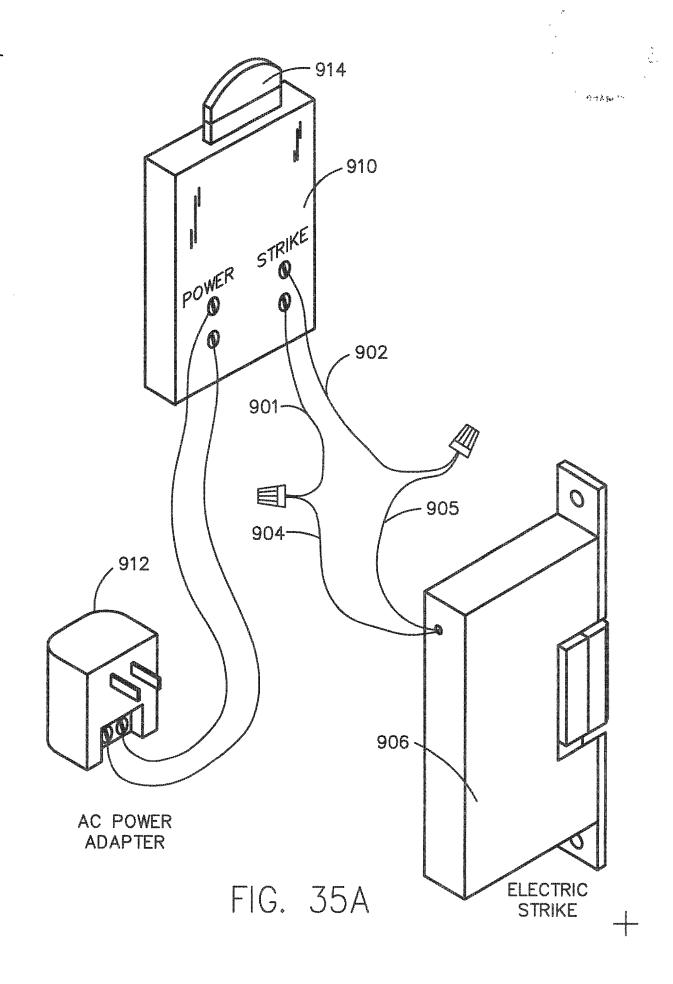
FIG. 34



PROXIMITY CARD READER







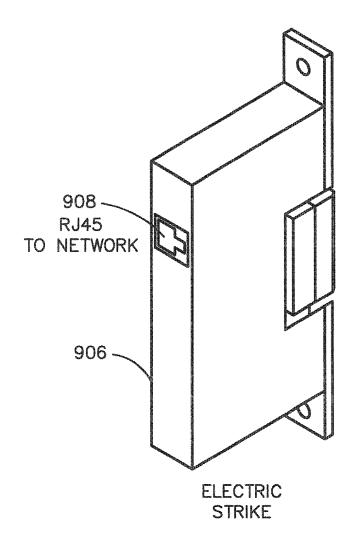


FIG. 35B

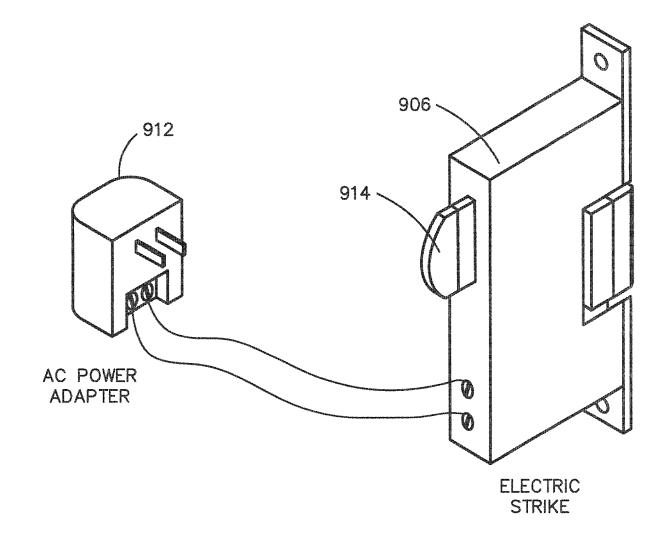
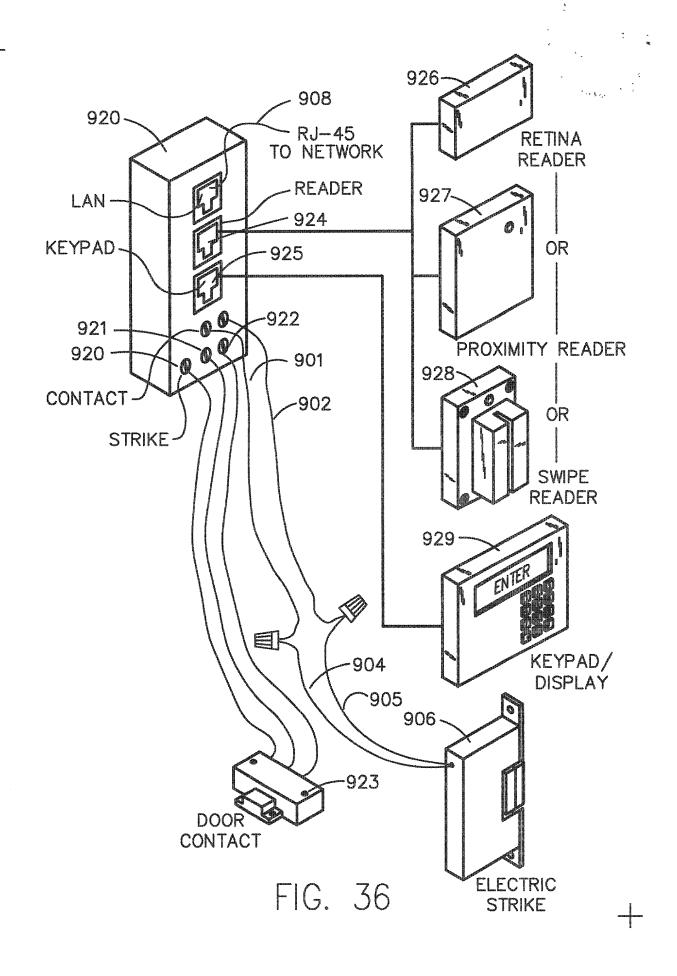
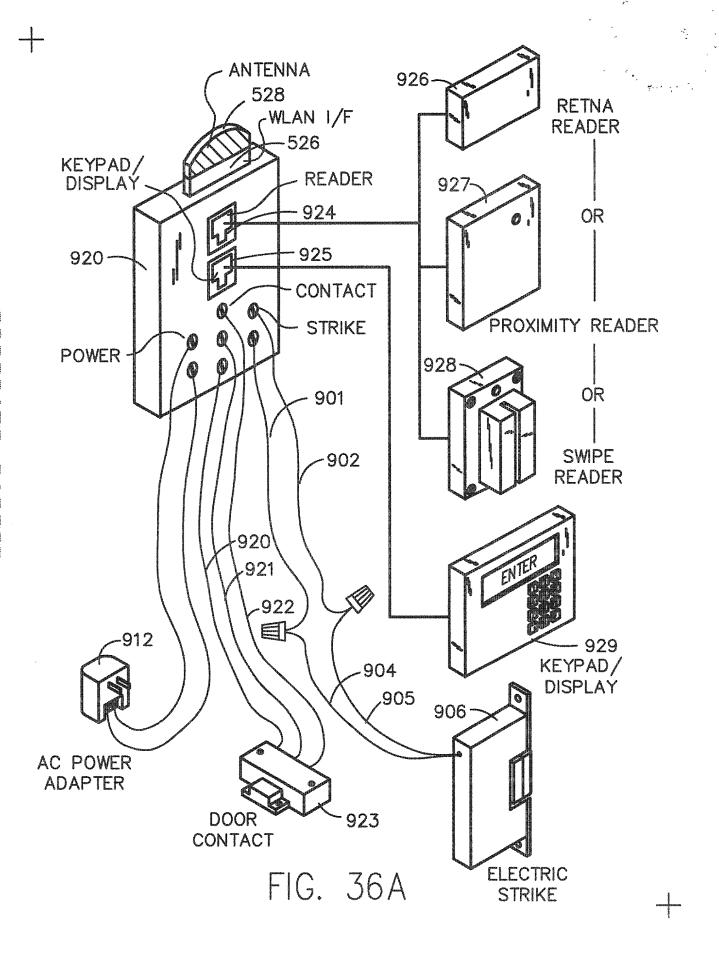
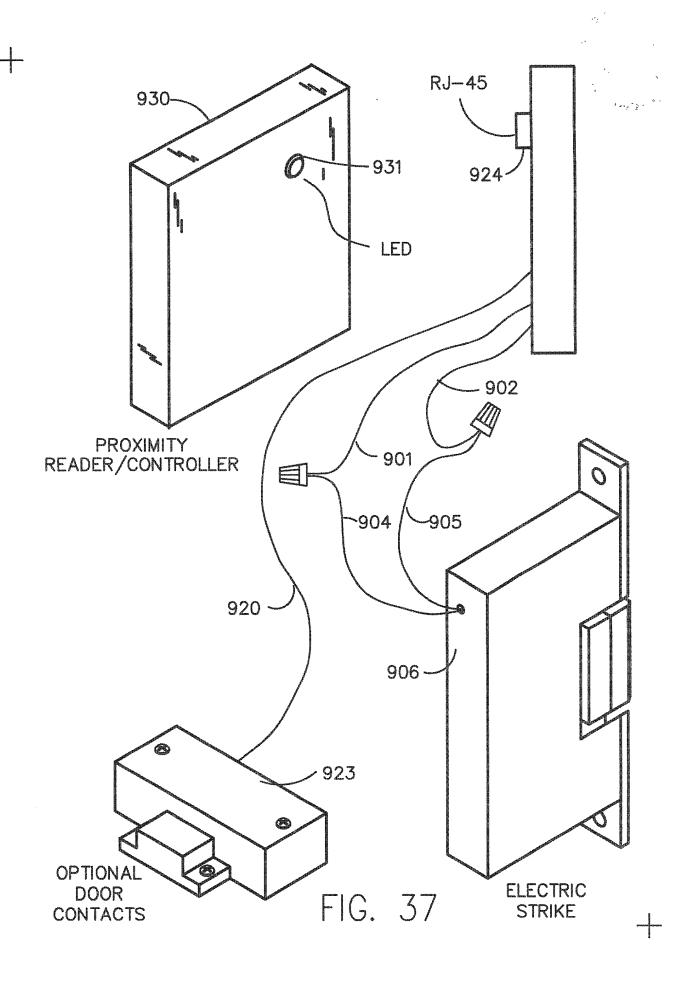
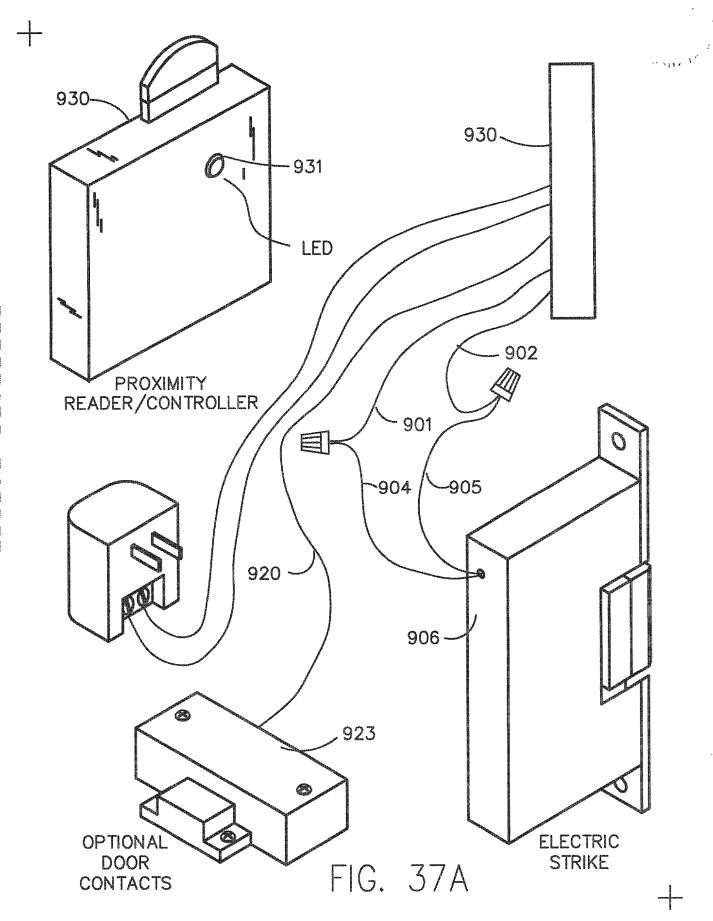


FIG. 35C









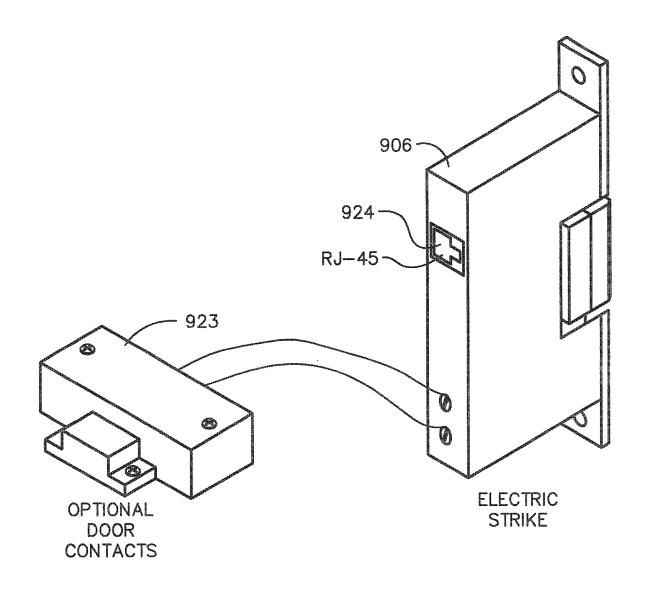


FIG. 37B

-

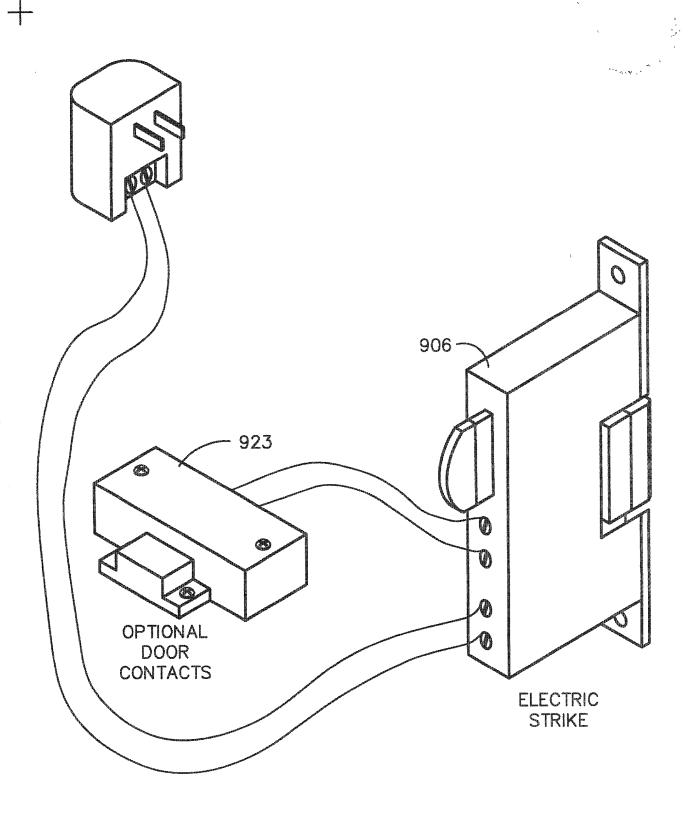
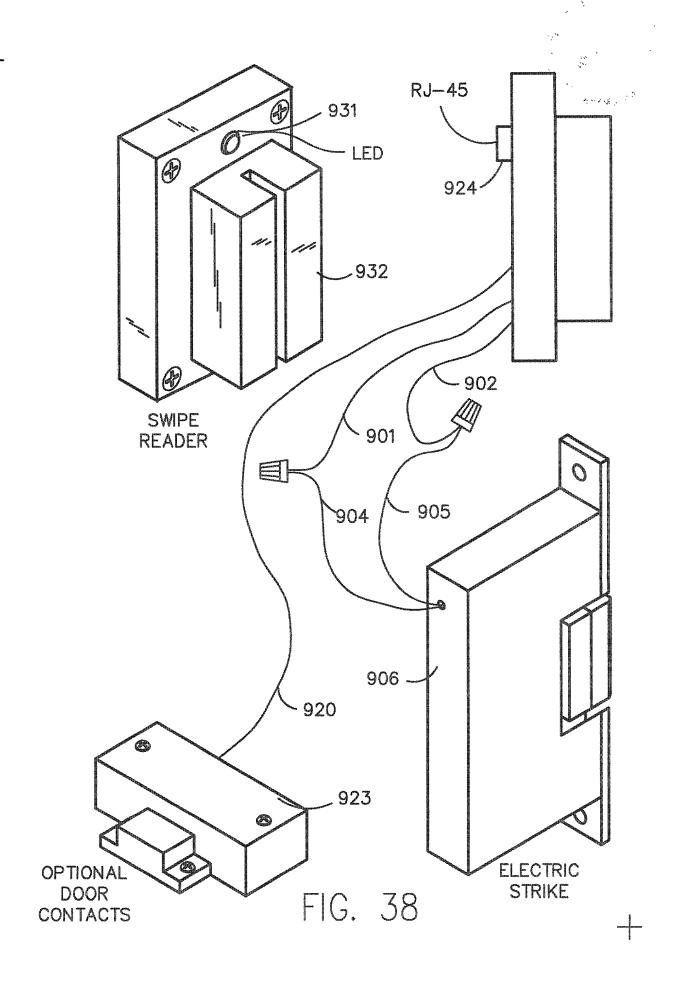
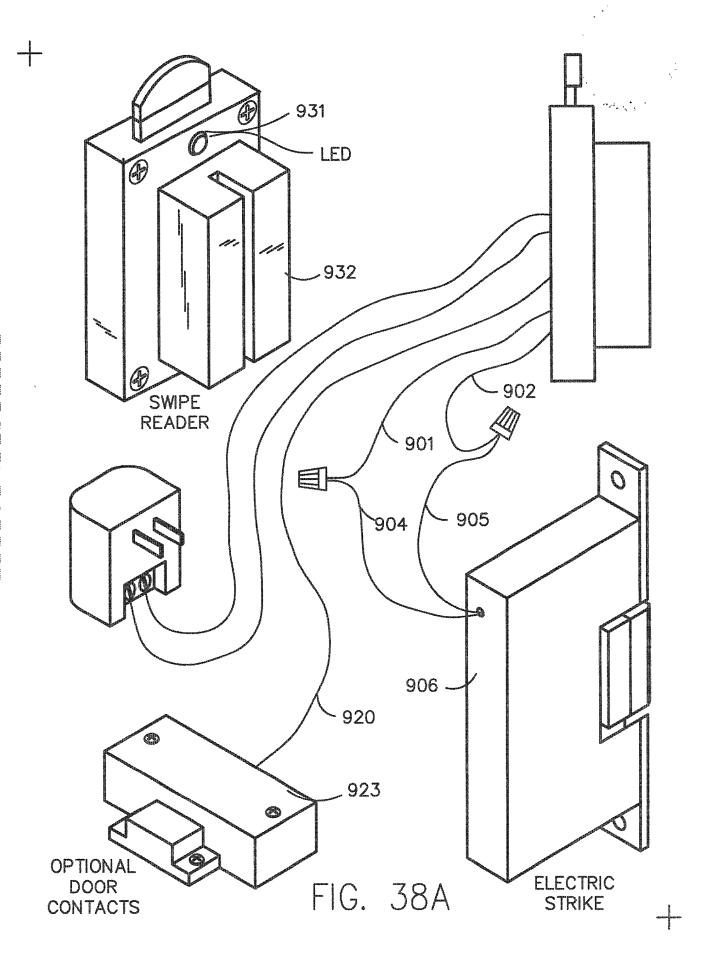
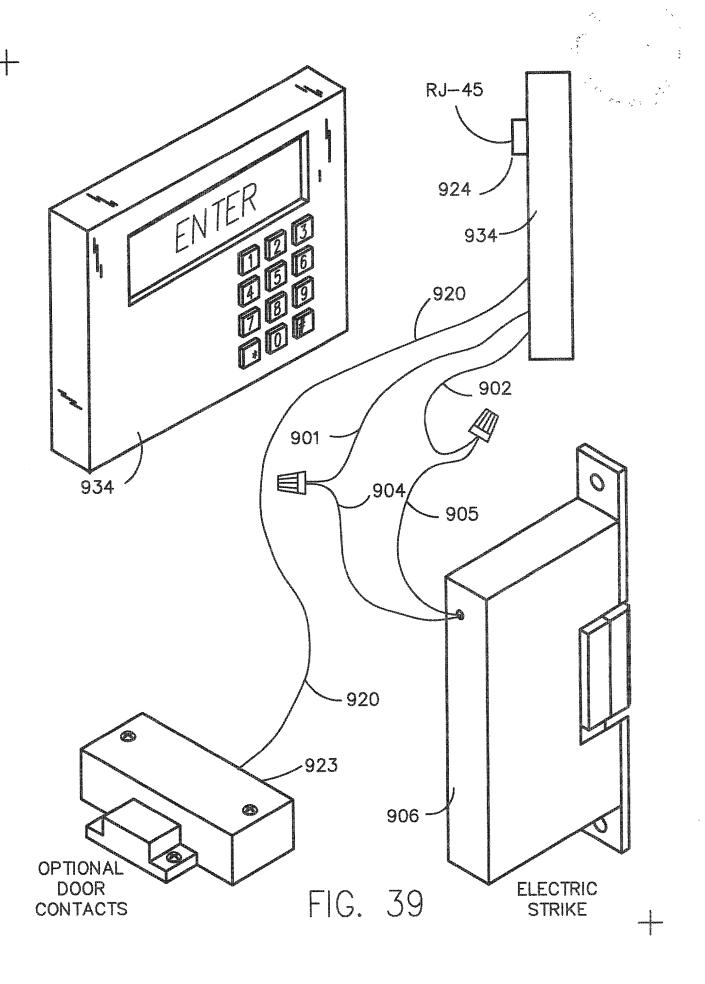
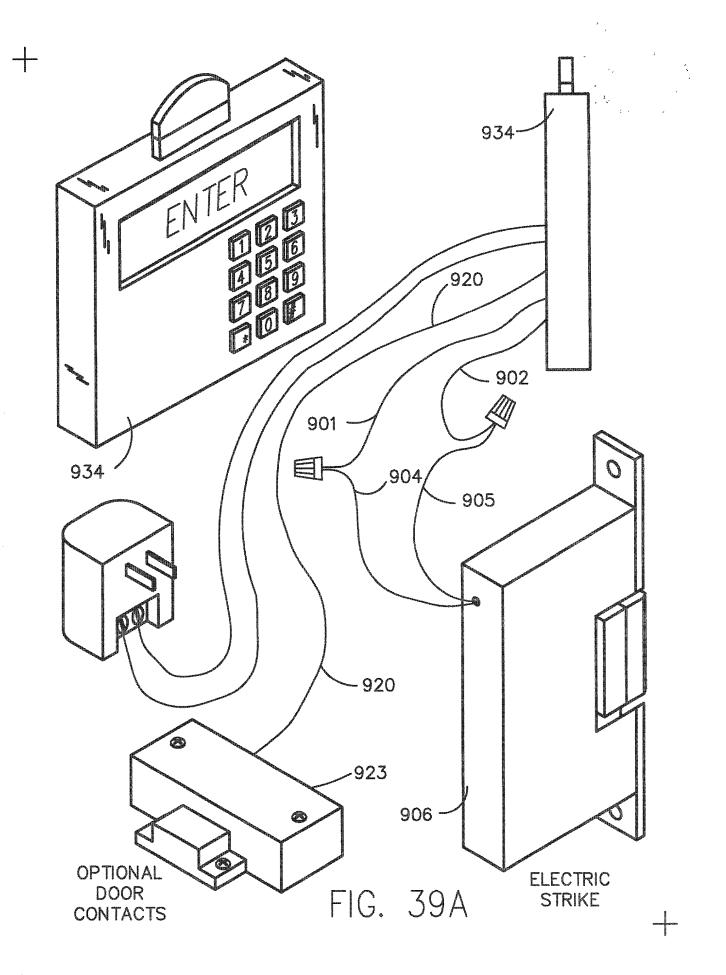


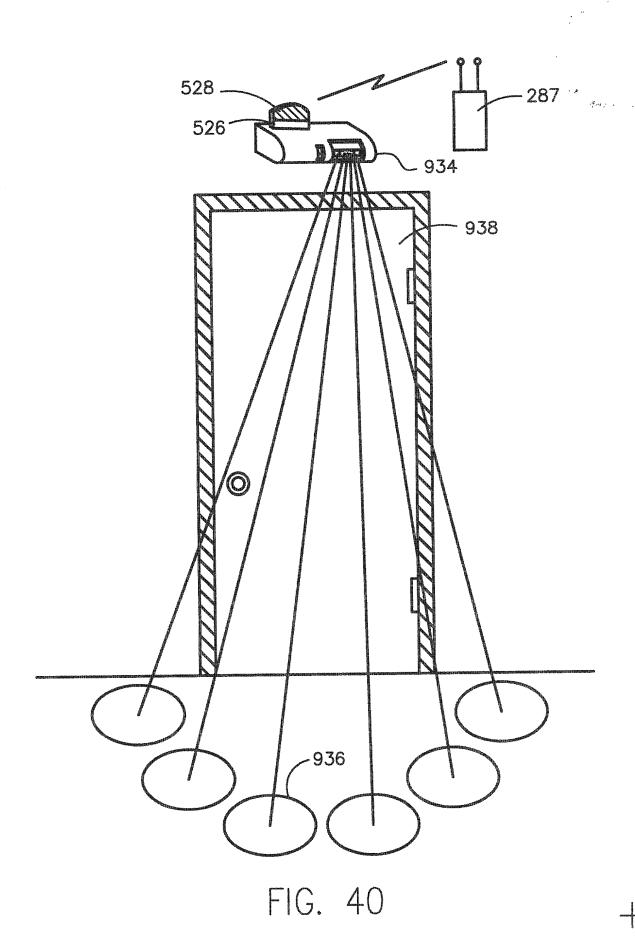
FIG. 37C

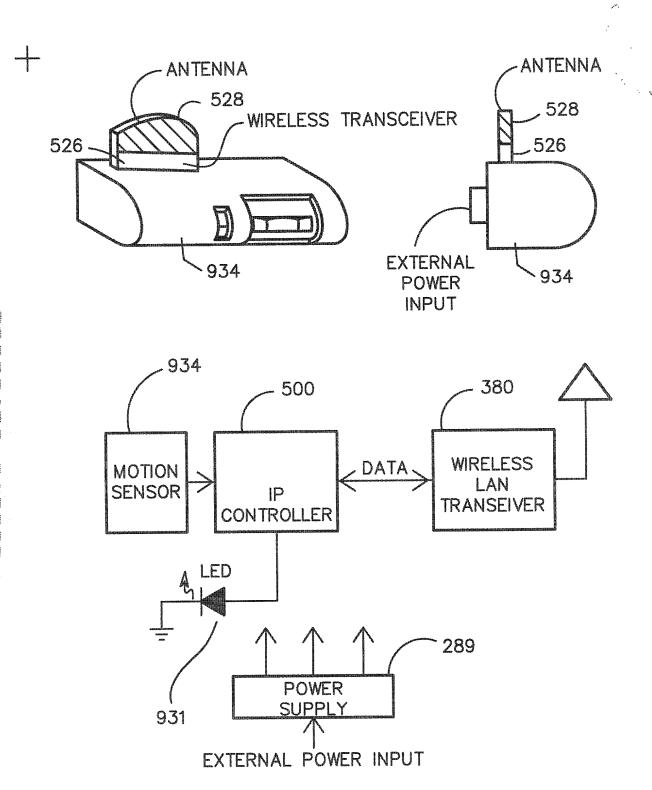






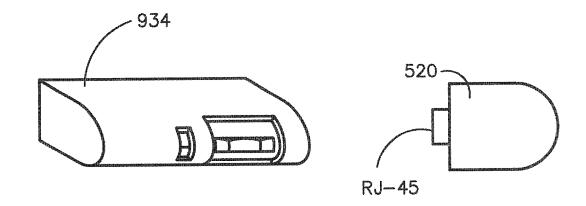






WIRELESS EXIT SENSOR FIG. 40A

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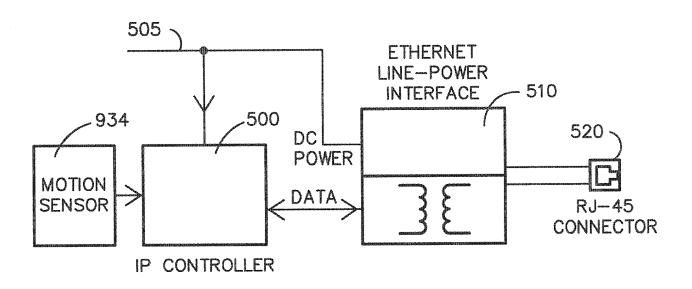


FIG. 40B

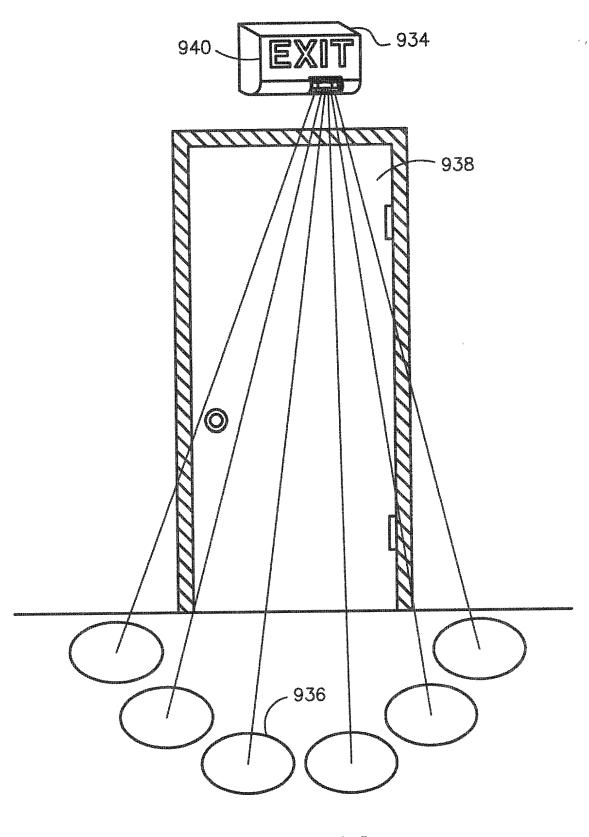
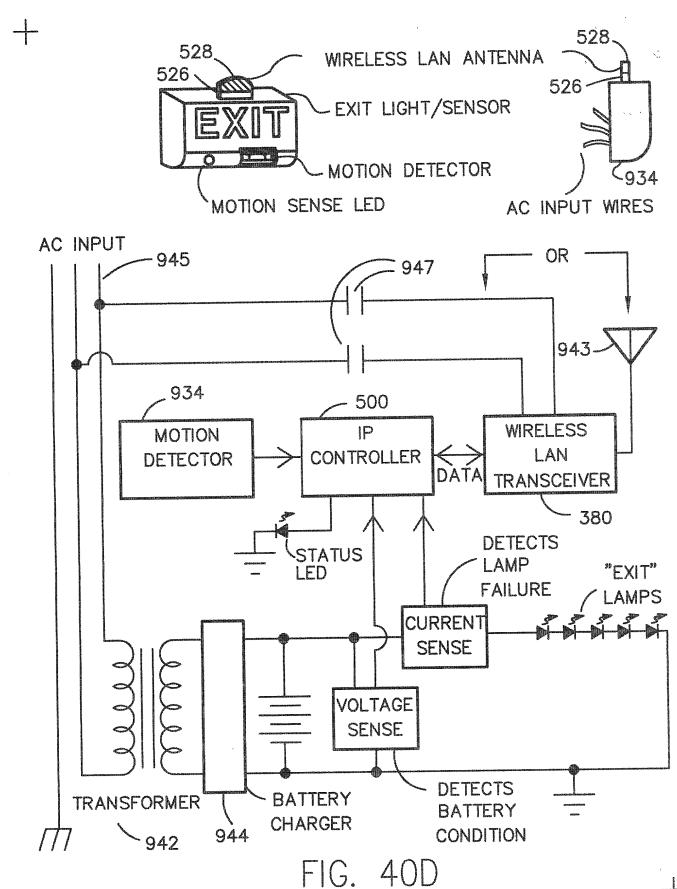
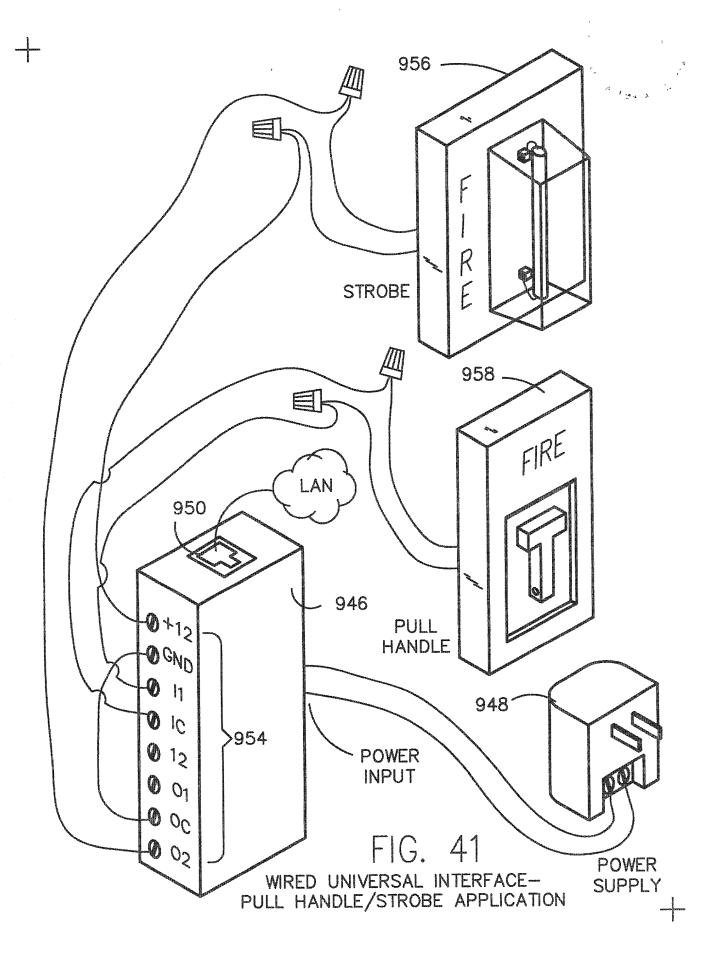
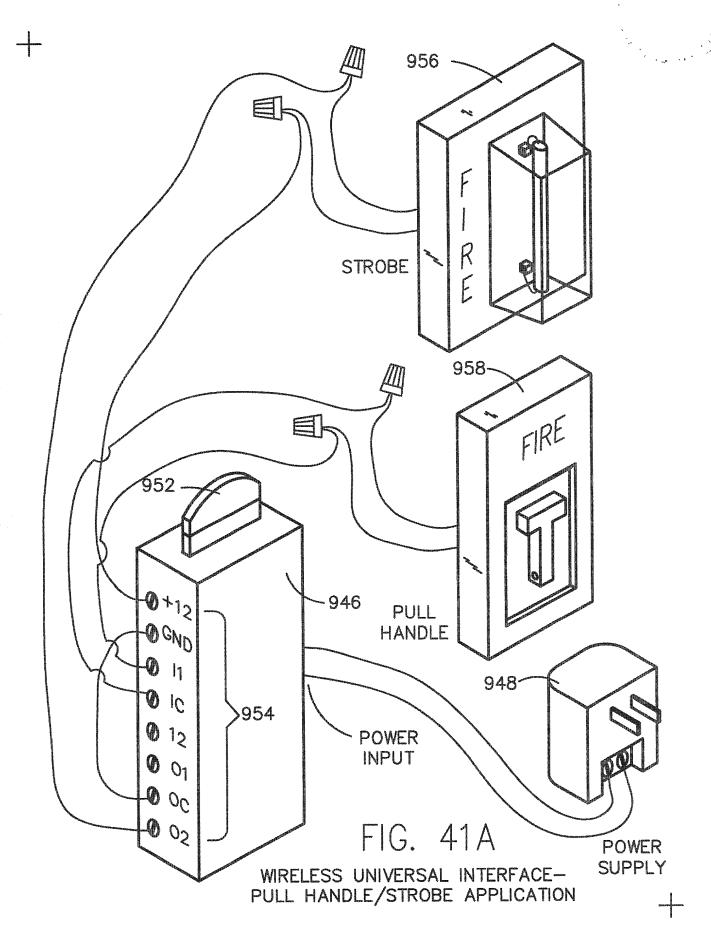


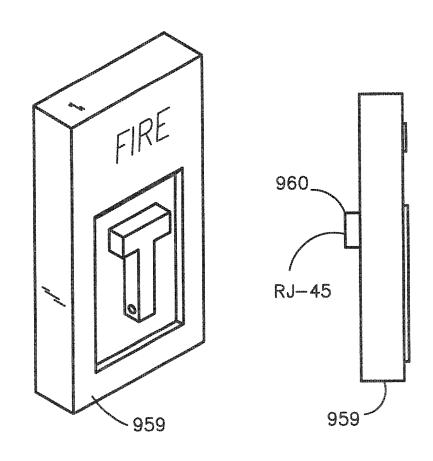
FIG. 40C

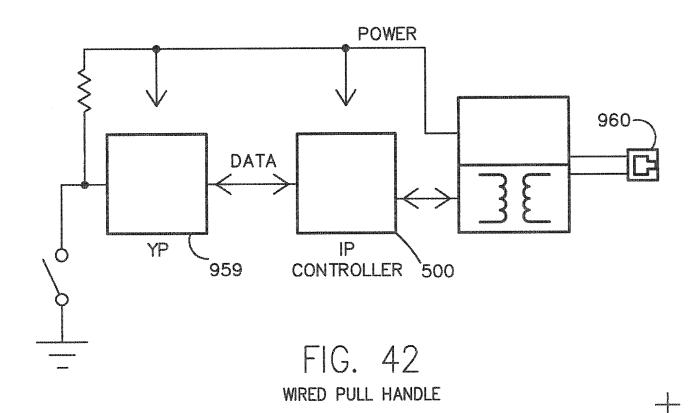
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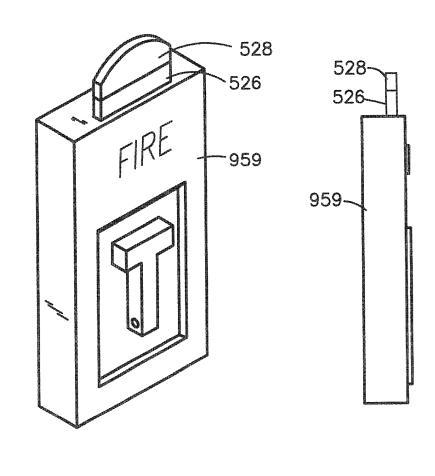


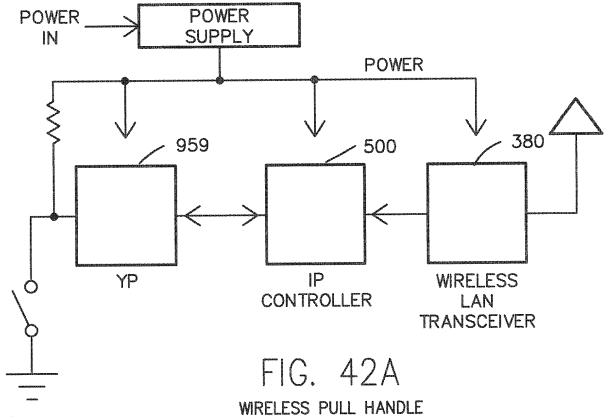


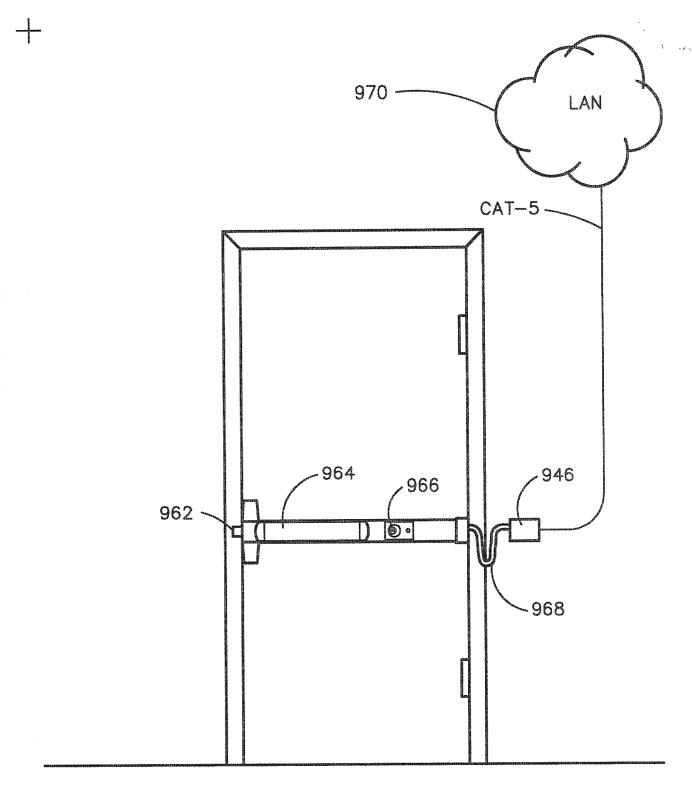






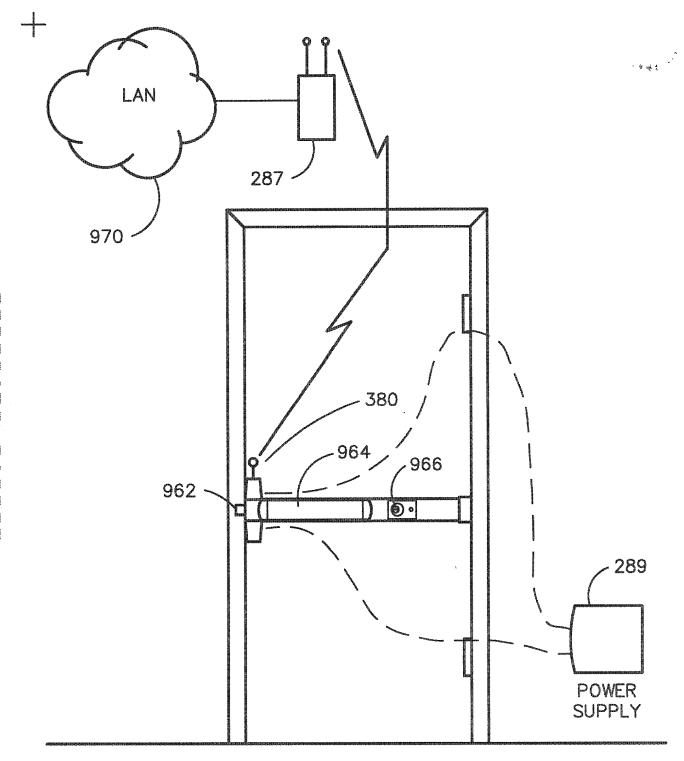






WIRED EXIT DEVICE

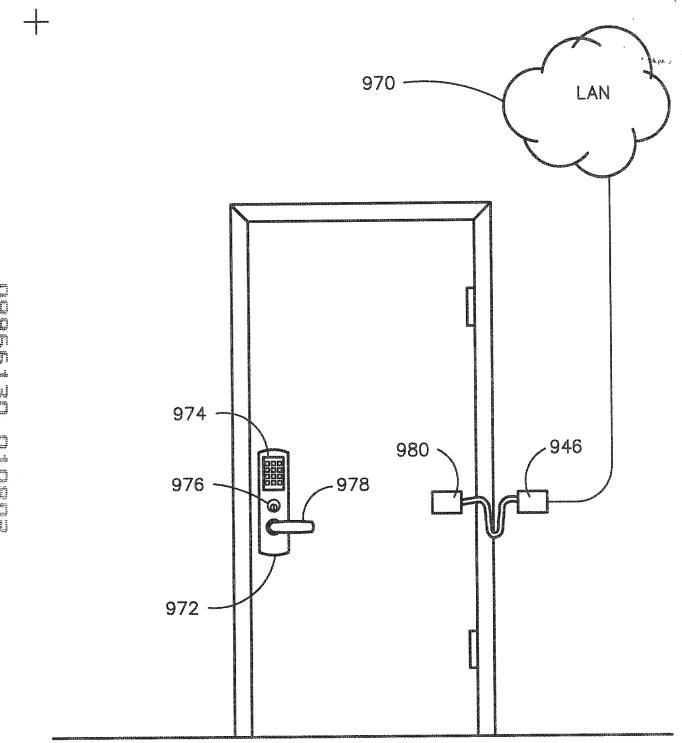
FIG. 43



WIRELESS EXIT DEVICE

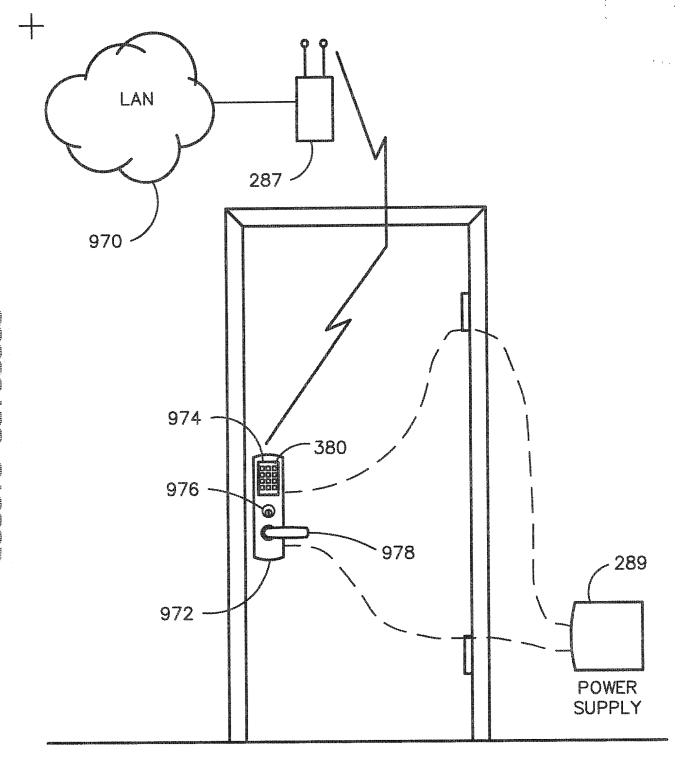
FIG. 43A

--



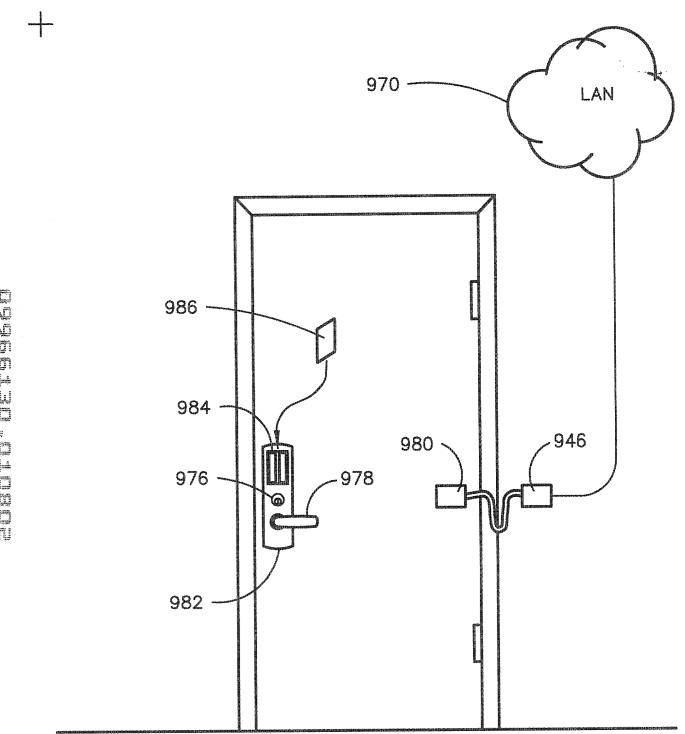
WIRED KEYPAD MORTISE LOCK

FIG. 44



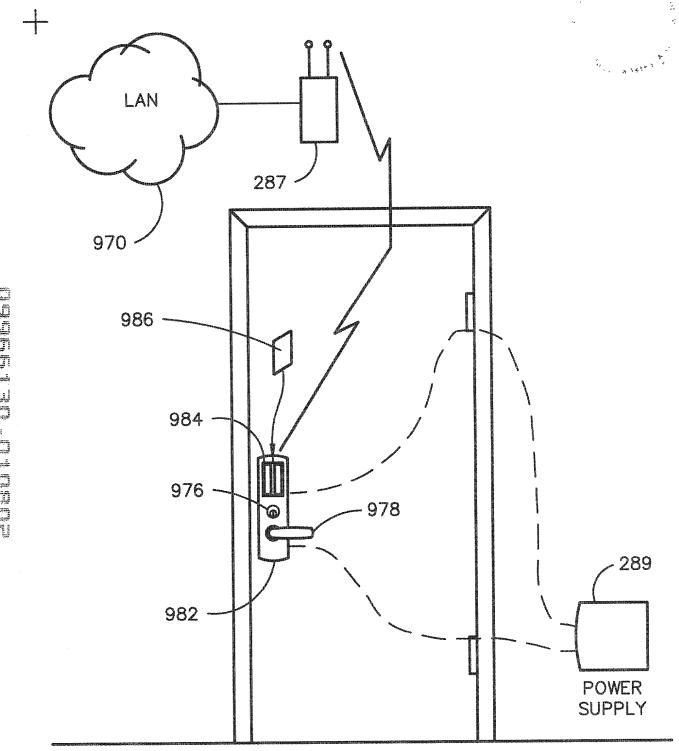
WIRELESS KEYPAD MORTISE LOCK

FIG. 44A



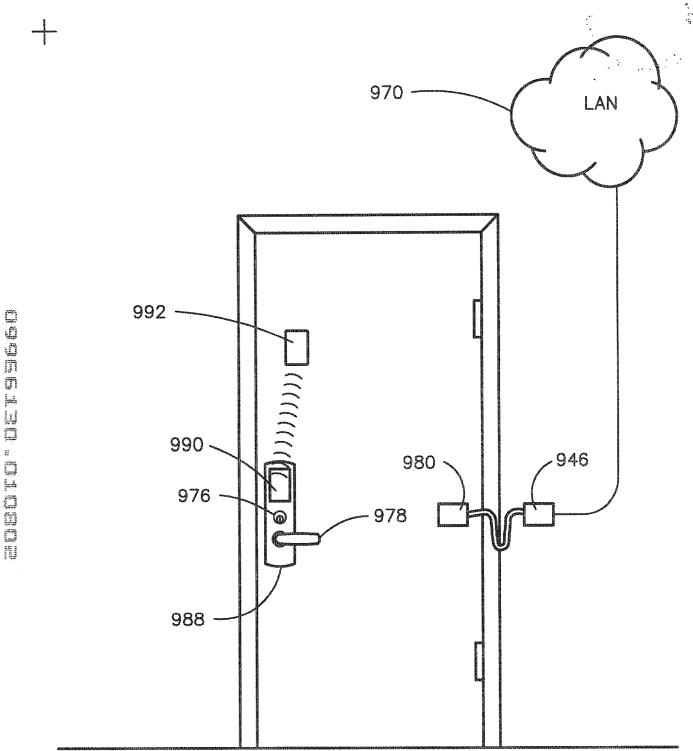
WIRED SWIPE CARD READER MORTISE

FIG. 45



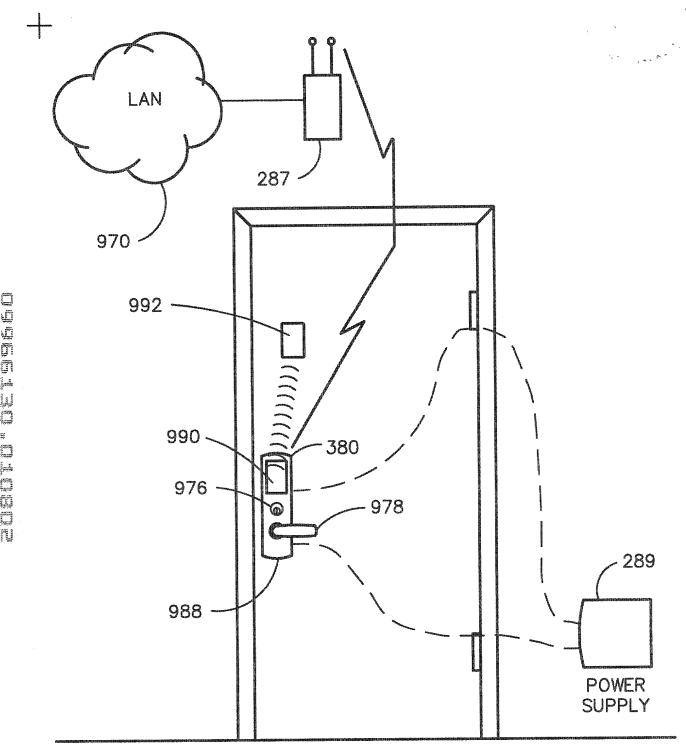
WIRELESS SWIPE CARD MORTISE LOCK

FIG. 45A



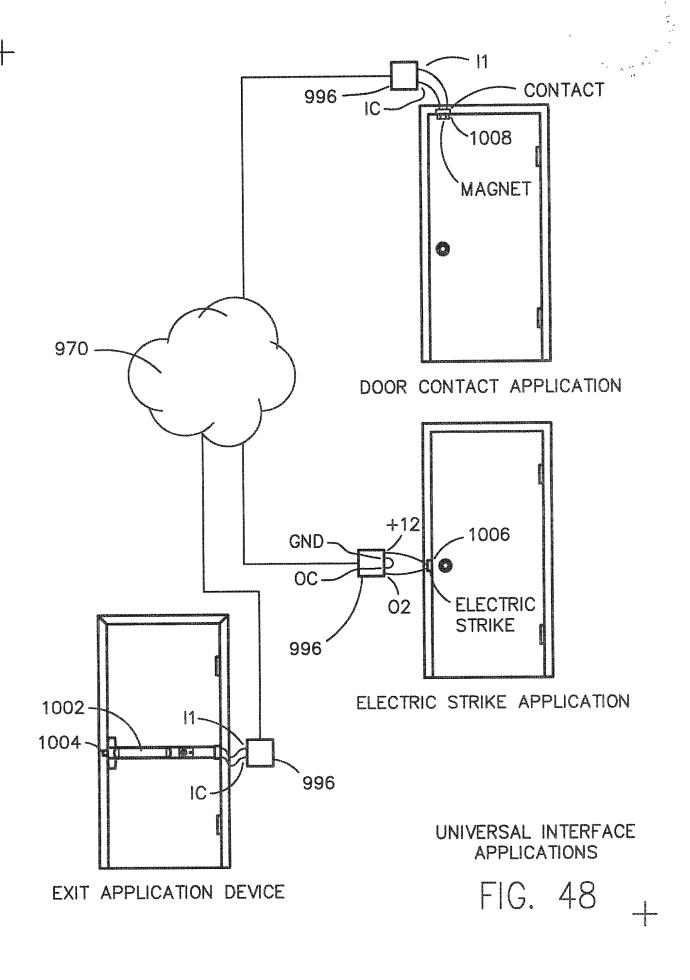
WIRED PROXIMITY CARD READER MORTISE LOCK

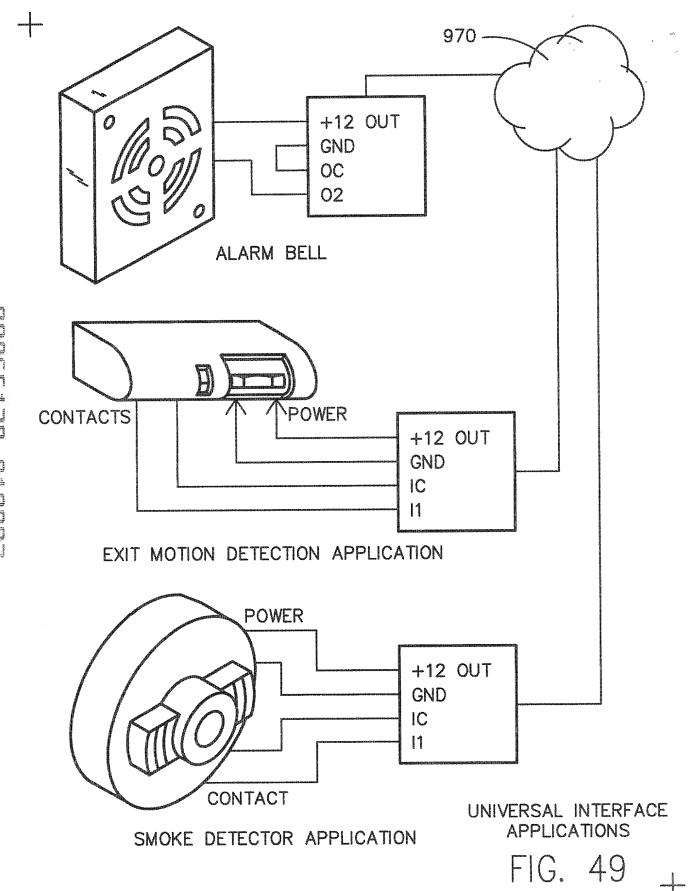
FIG. 46

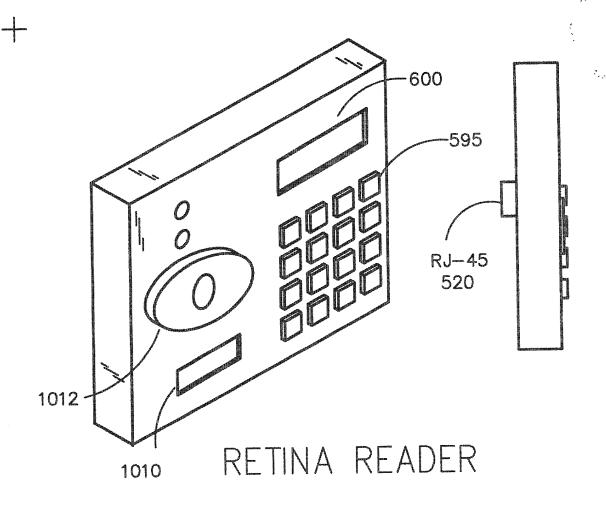


WIRELESS PROXIMITY CARD READER MORTISE LOCK

FIG. 46A







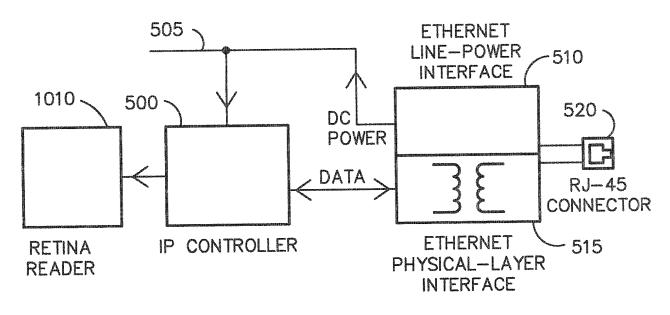
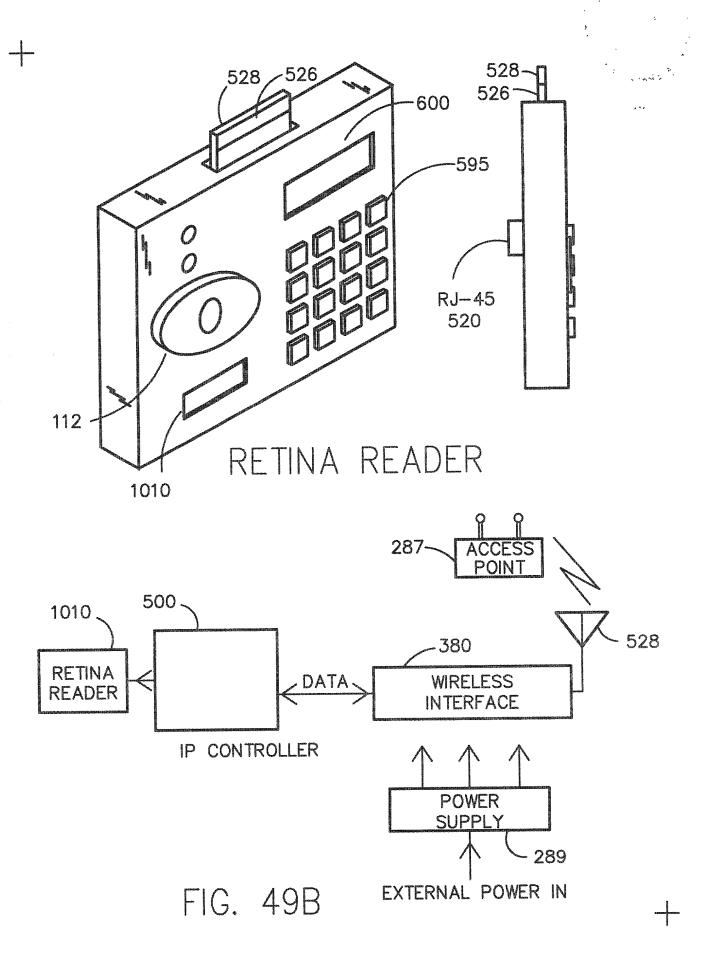
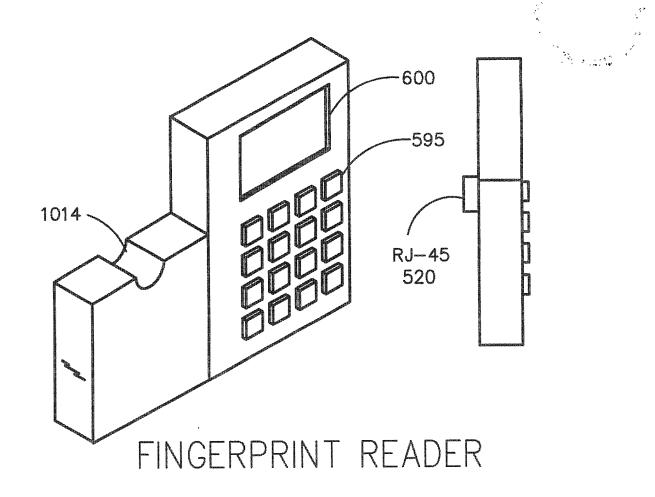


FIG. 49A

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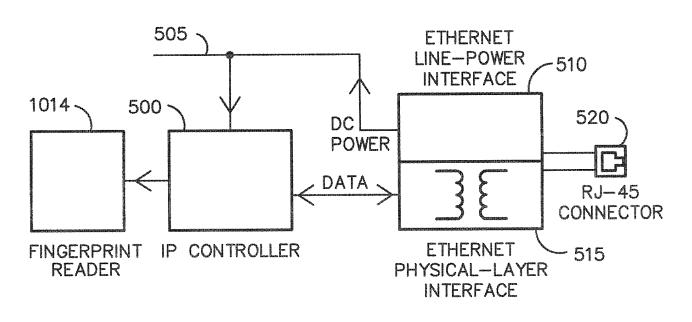


FIG. 49C

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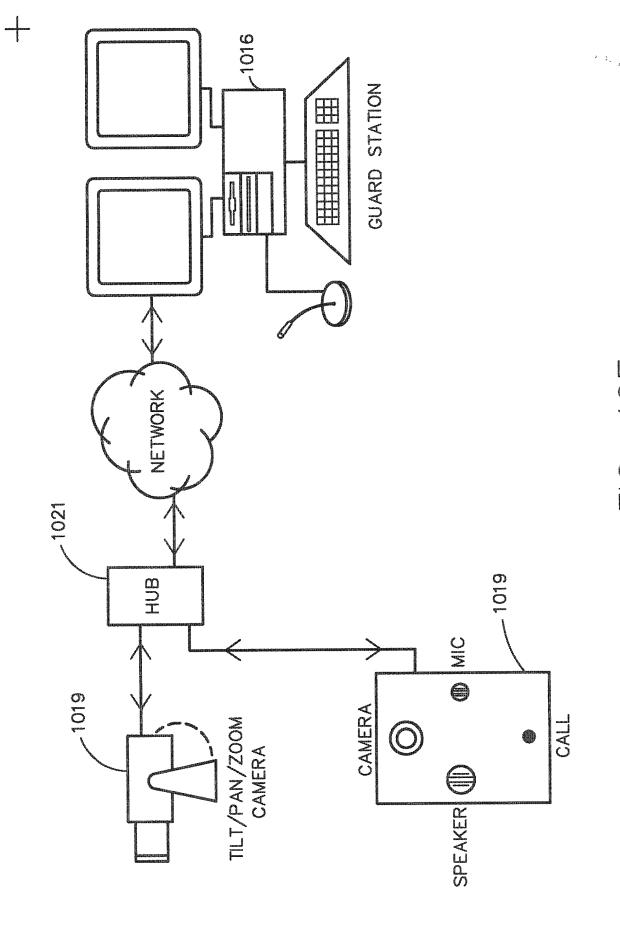
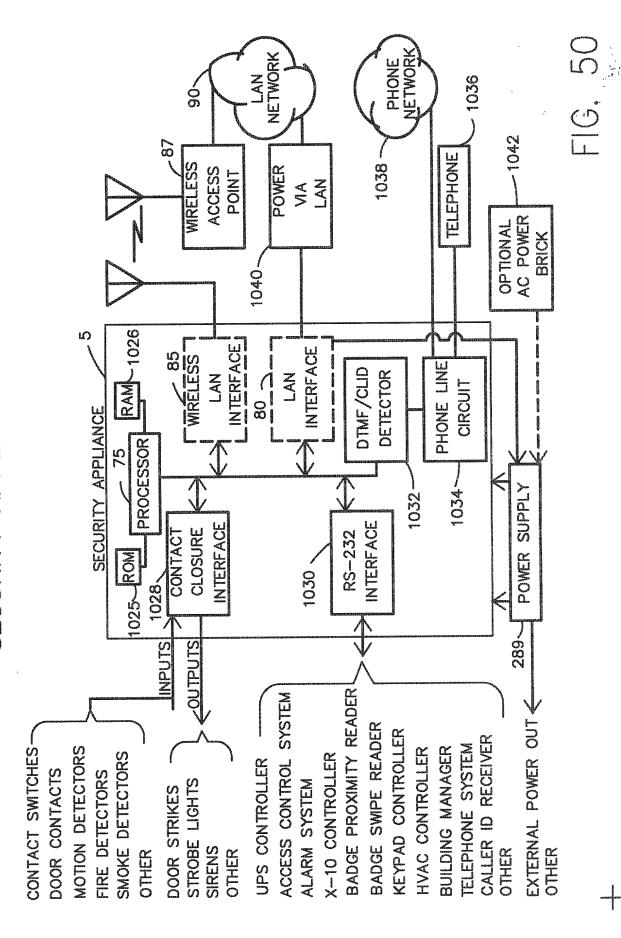
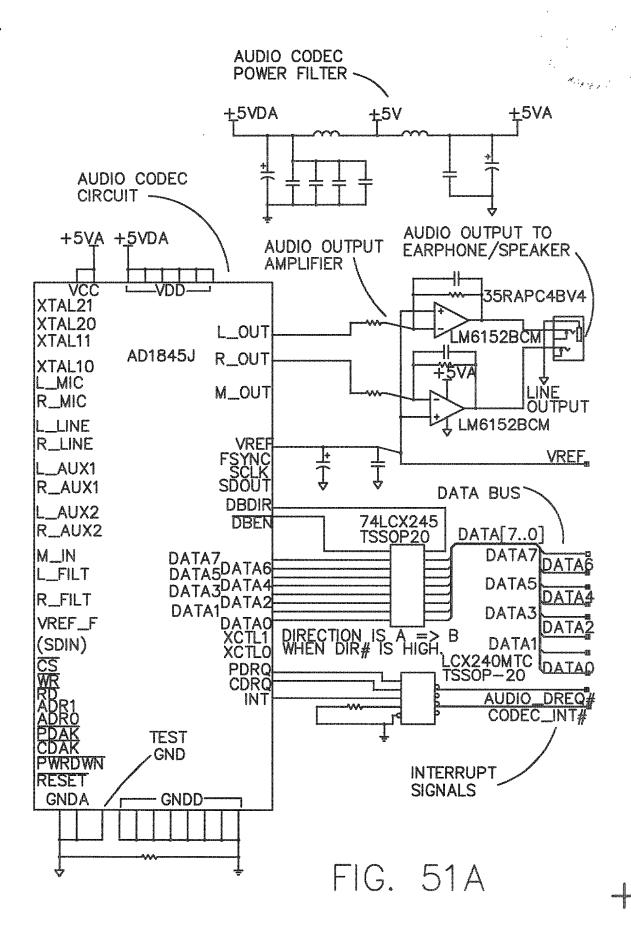


FIG. 49F

## SECURITY APPLIANCE BLOCK DIAGRAM





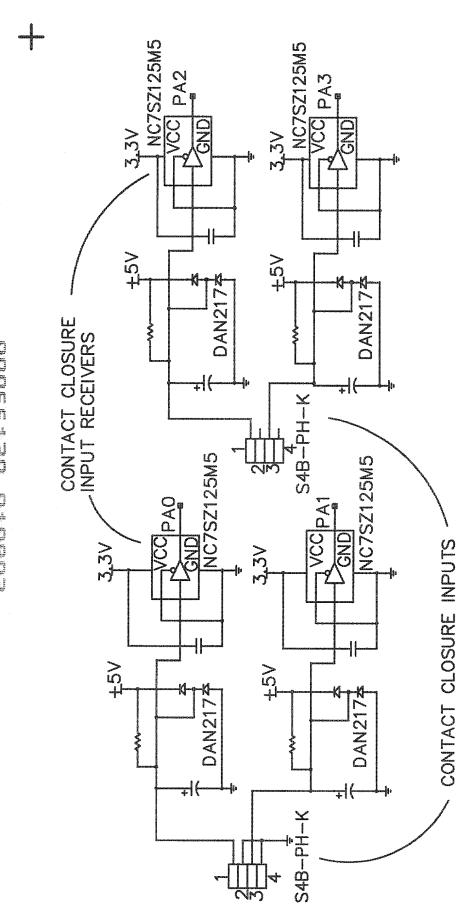
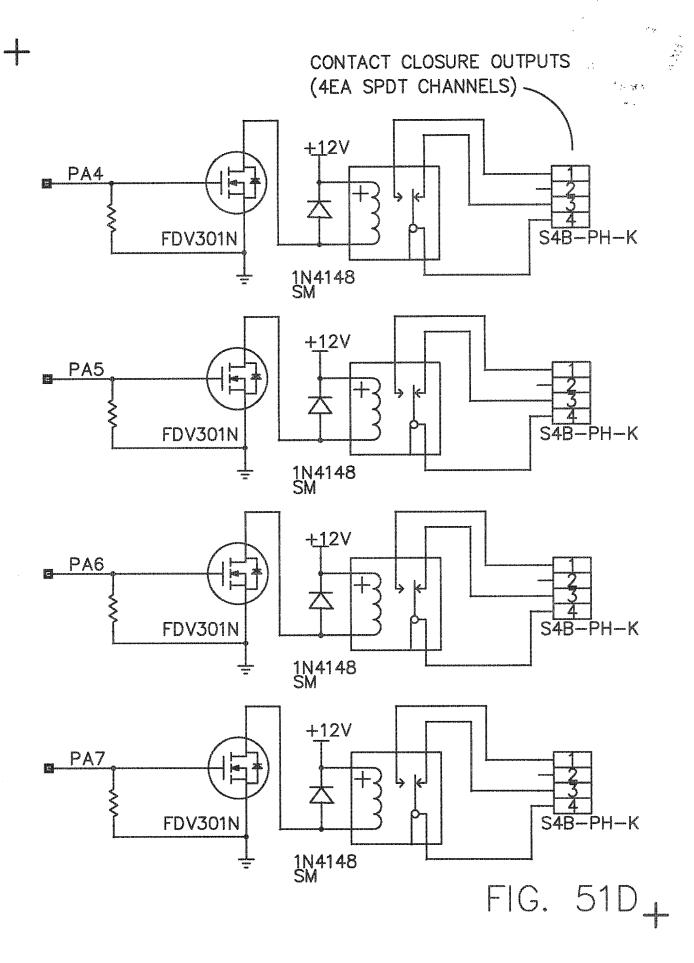
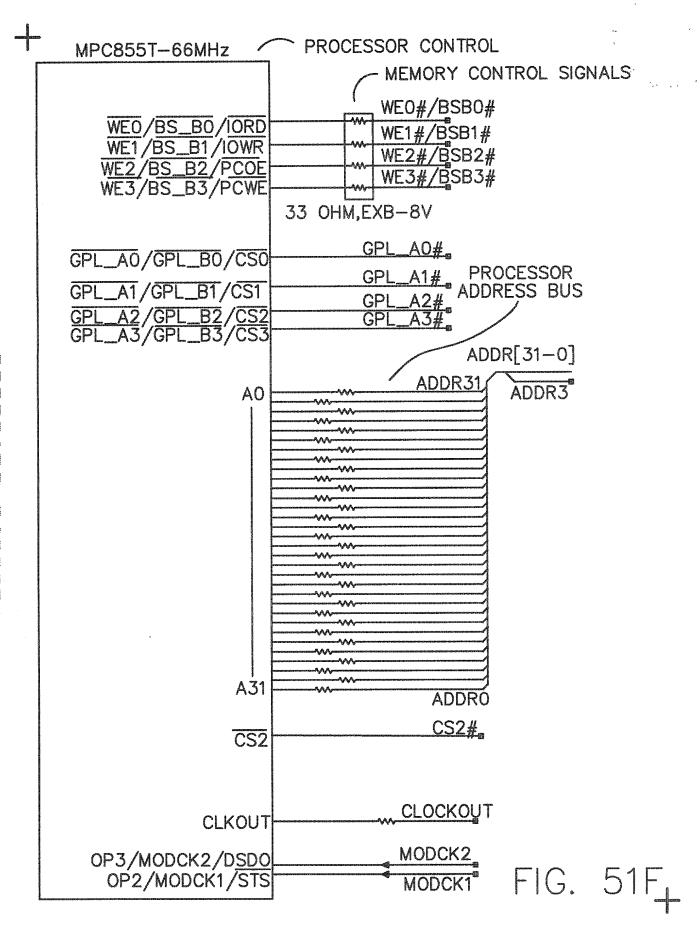
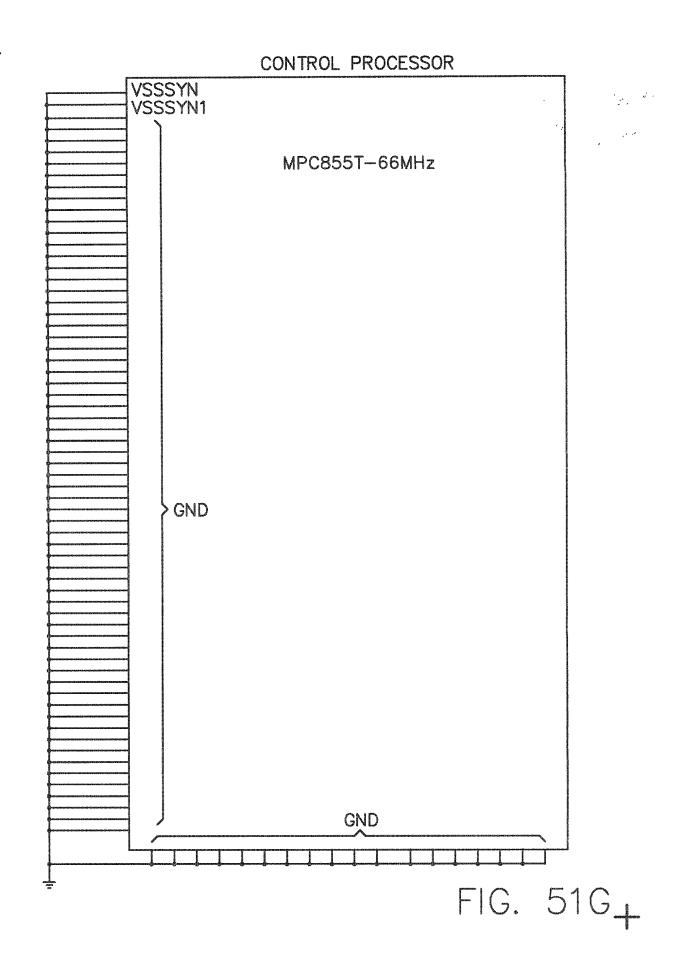


FIG. 51C +







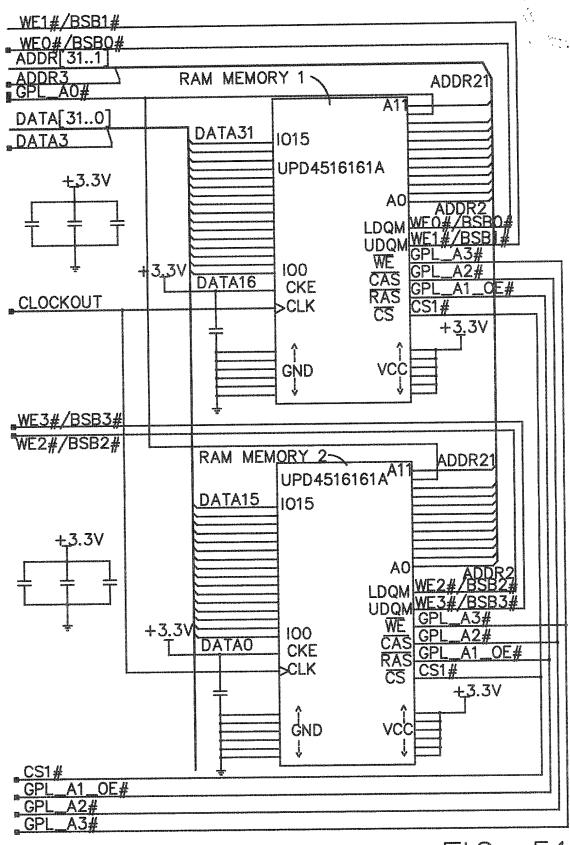


FIG. 511 +

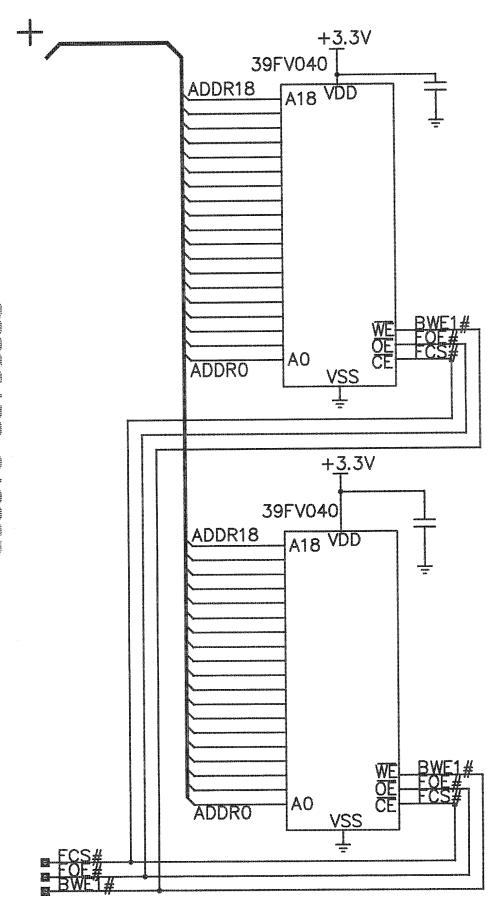
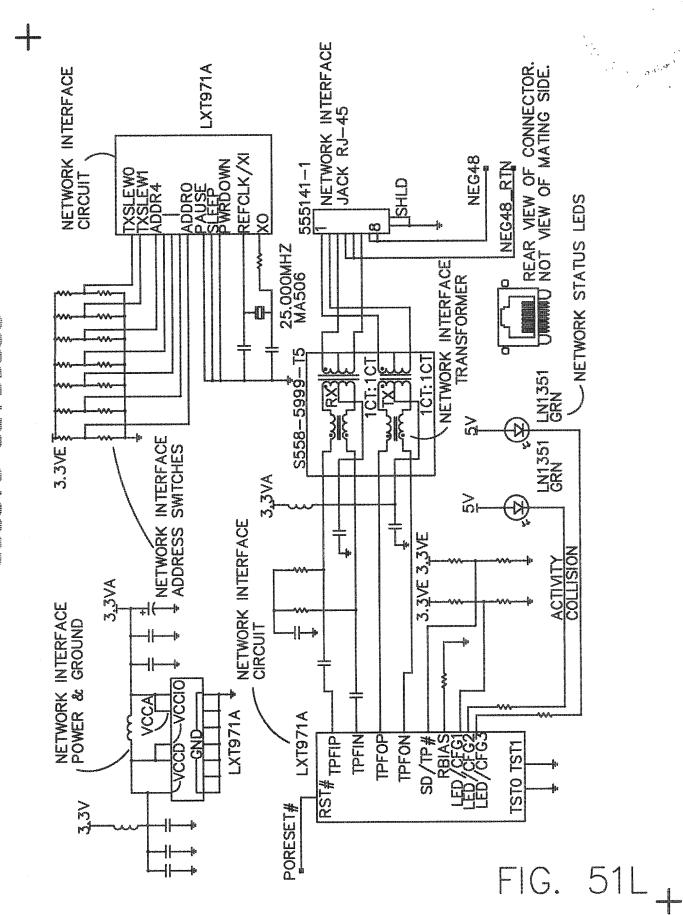
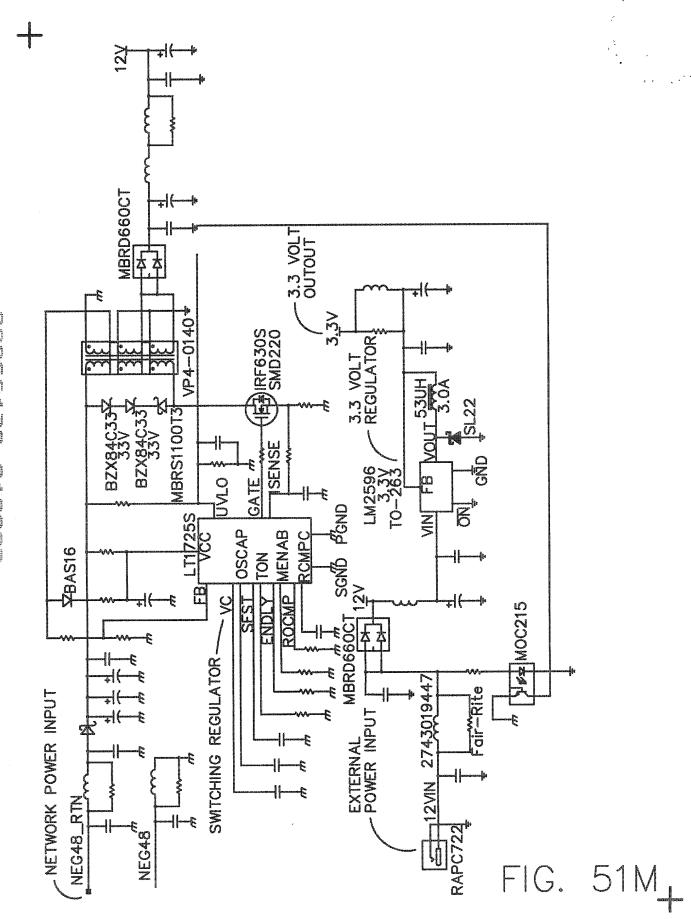


FIG. 51J<sub>+</sub>

| CONTROL MICROPROCESSOR        |  |
|-------------------------------|--|
| MPC855T-66MHz                 |  |
| PD3/MII_TXD1                  | PD4/MII_TXD2   |
| PD5/MII_TXD3                  |  |
| PD7/MII_RX_ERR                |  |
| PD9/MII_TXD0                  |  |
| PD11/MII_TX_ER                | EDIO/MII NADO  |
| PD13/L1TSYNCB/MII_RXD1        | PD14/L1RSYNCA/MII RXD2   |
| PD15/L1TSYNCA/MII_RXD3        | MII_CRS  |
| MII_MDIO                      |  |
| MII_COL                       | MII_TX_EN  |
| PVI 31 annua GV GU Sun        | IRQ7/MII_TX_CLK  |
|                               |  |
|                               |  |
|                               |  |
|                               |  |
|                               |  |
|                               | ON THE PROPERTY OF THE PROPERT |
|                               |  |
| PB23/SMSYN1/SDACK1            |  |
| PA15/RXD1                     |  |
| PA14/TXD1                     |  |
|                               | ver und nicht auf der Schale der  |
|                               | PA7/CLK1/TIN1/L1RCLKA/BRG01  |
| PA6/CLK2/TOUT1/BRGCLK1        | DAS /CLUZ /TINO /LITCLUX /DDC  |
| PA4/CLK4/TOUT2                | PA3/CLK5/TIN3/BGROUT3  |
| PA2/CLK6/TOUT3/L1RCLK/BRGCLK2 |  |
| PAO/CLK8/TOUT4/L1TCLKB        | I FAI / CLR / / IIIAT / DONOT _  |
| PAO/CLRO/10014/LITCLRD        | Sporter Entransporter and Entransporter (Education and Education and Edu |

FIG. 51K +





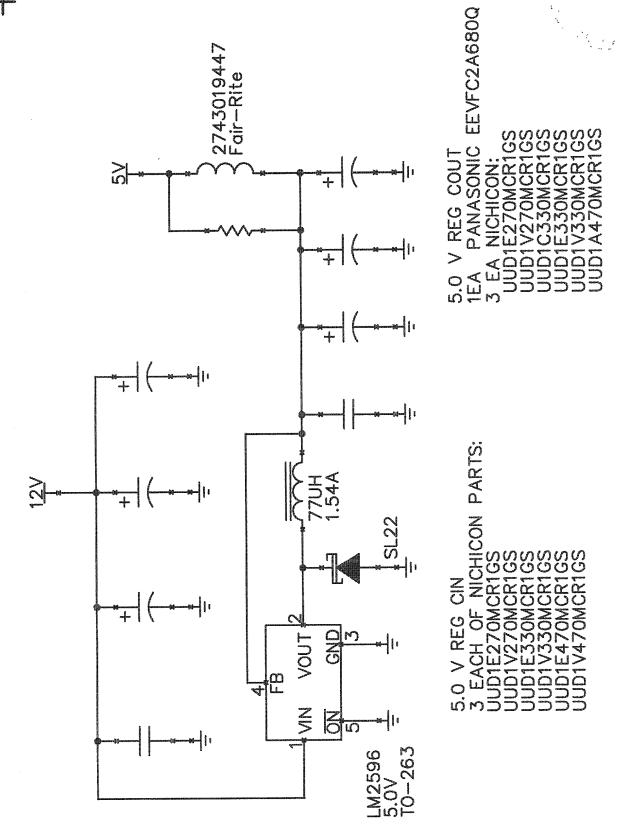


FIG. 51N

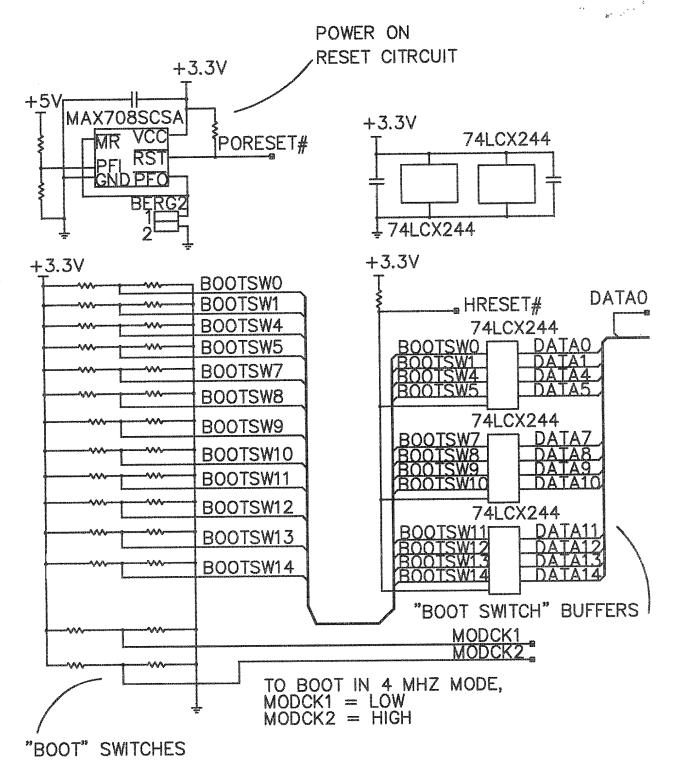


FIG. 510

+

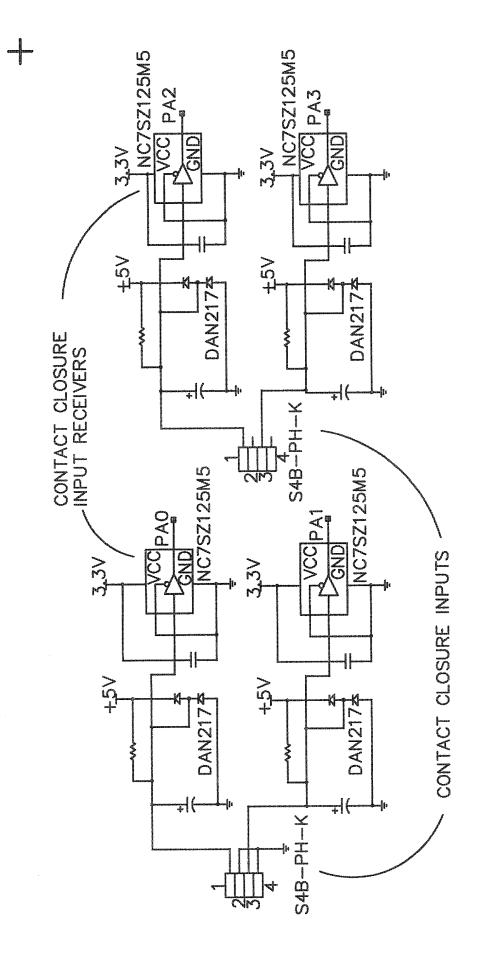
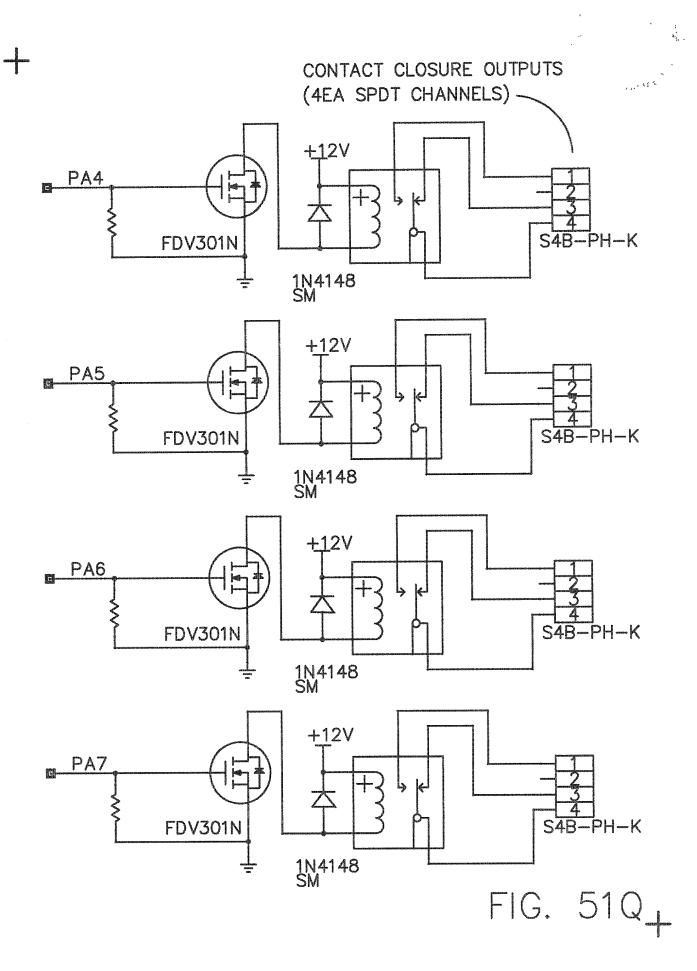
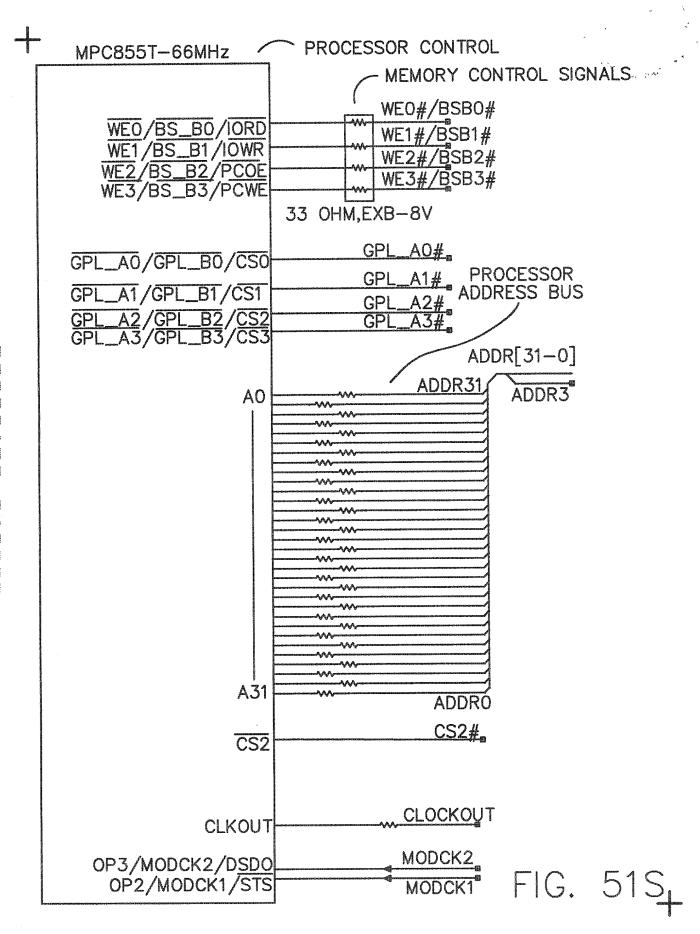
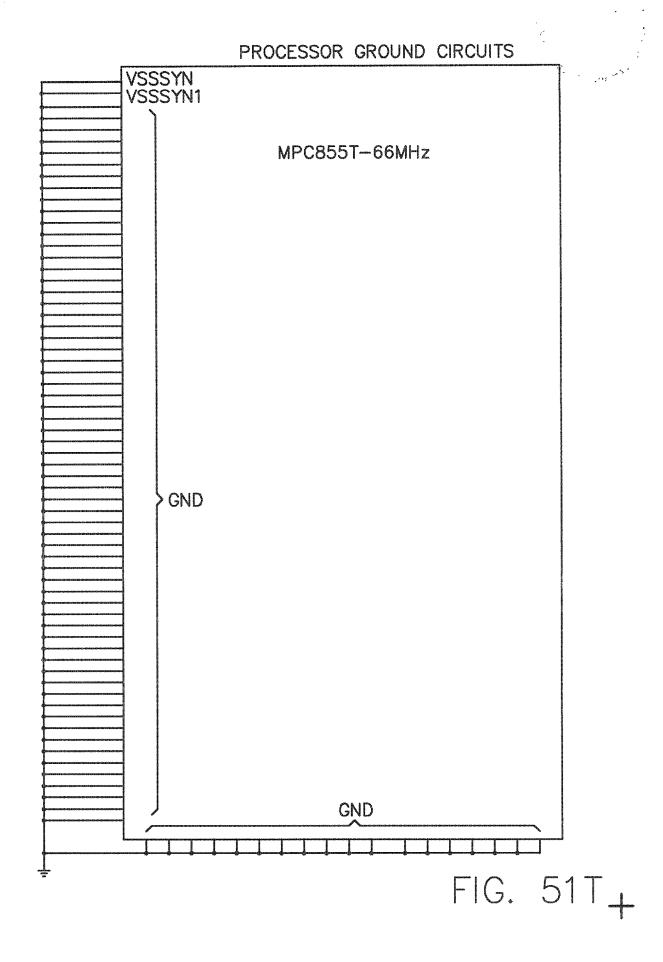


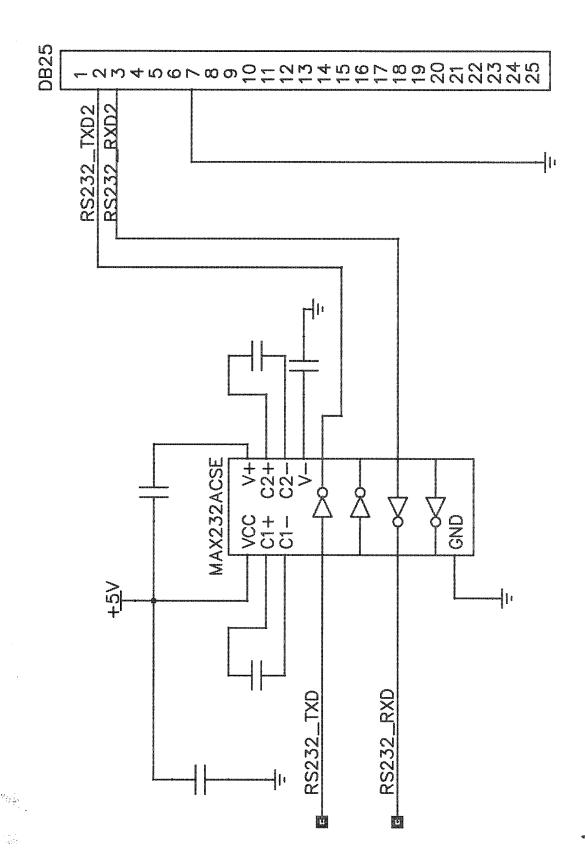
FIG. 51P+







EIC. SIV+



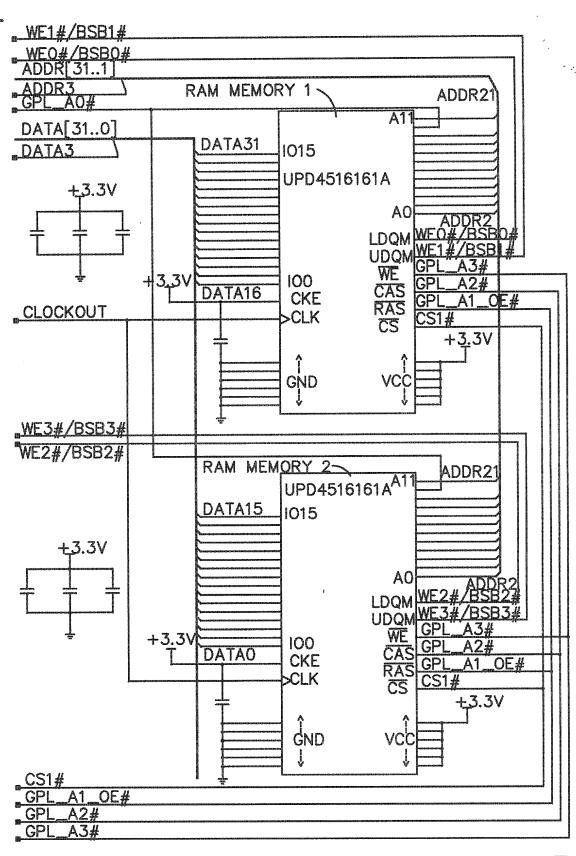


FIG. 51W+

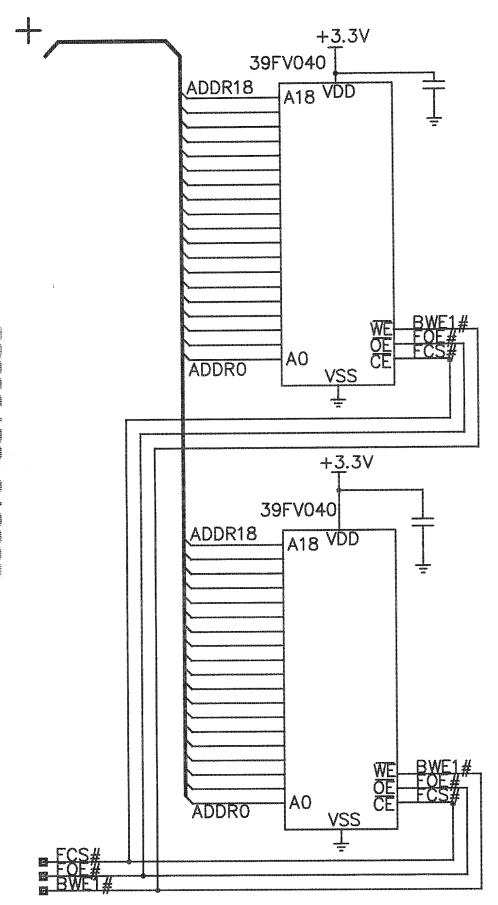
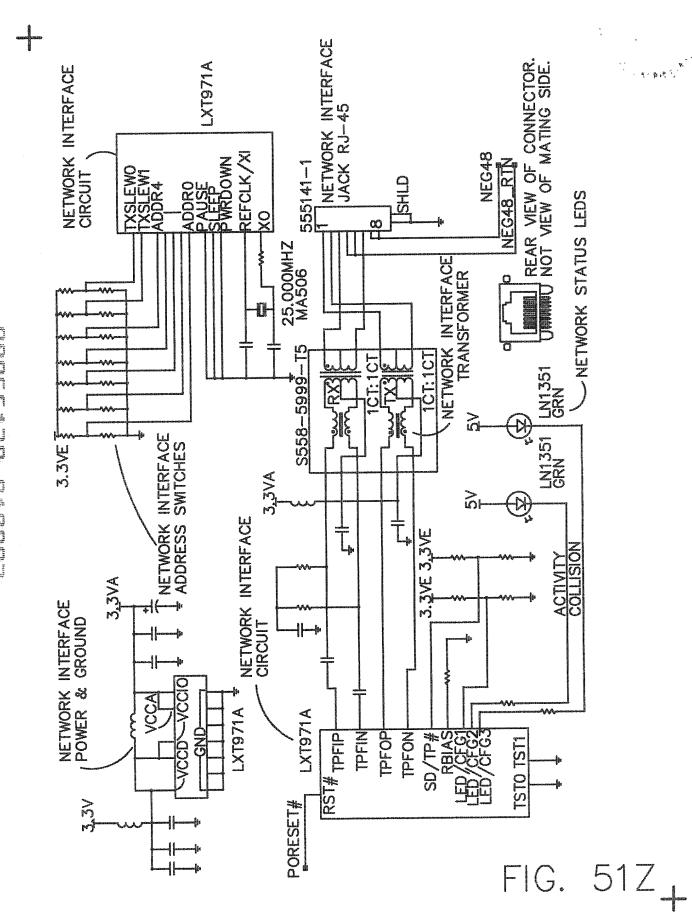
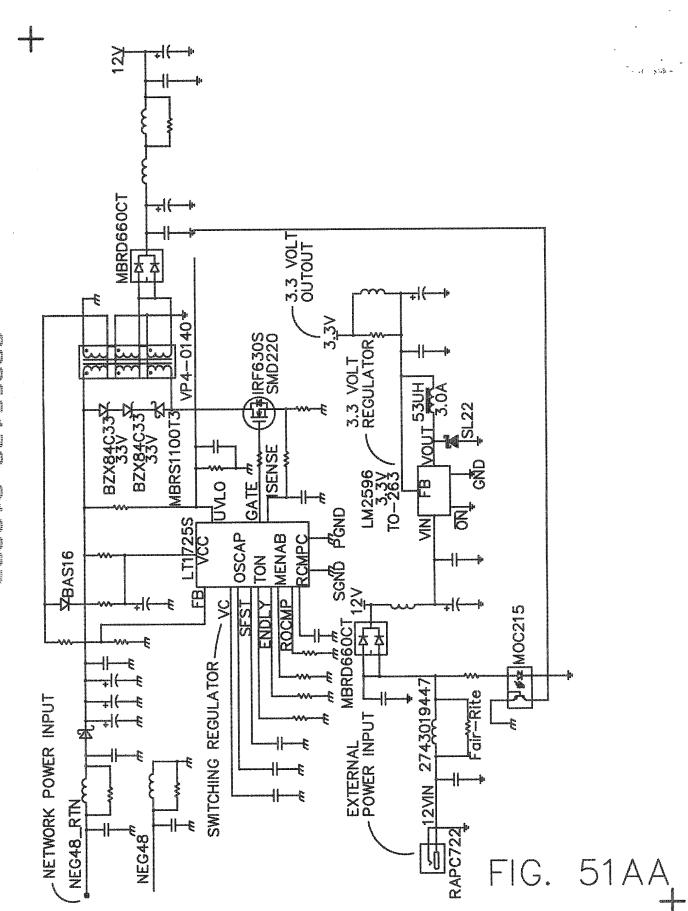


FIG. 51X+

| CONTROL MICROPROCESSOR   |                                     |
|--|-------------------------------------|
| MPC855T-66MHz  |                                     |
| PD3/MII_TXD1   | PD4/MII_TXD2                        |
| PD5/MII_TXD3   | PD6/MIL RXDV                        |
| PD7/MII_RX_ERR   | PD8/MII_RX_CLK                      |
| PD9/MII_TXD0   | PD10 /MIL RXD0                      |
| PD11/MII_TX_ER   | PD12/L1SYNCB/MIL_MDC                |
| PD13/L1TSYNCB/MII_RXD1   | PD14/L1RSYNCA/MIL RXD2              |
| PD15/L1TSYNCA/MII_RXD3   | MIL CRS                             |
| MII_MDIO   | MII TX EN                           |
| MII_COL  |                                     |
|  | IRQ7/MII_TX_CLK                     |
|  | ,                                   |
|  |                                     |
|  |                                     |
|  |                                     |
|  |                                     |
|  |                                     |
| PB23/SMSYN1/SDACK1   |                                     |
| · ·  |                                     |
| PA15/RXD1<br>PA14/TXD1   |                                     |
| ,  |                                     |
|  | DATE (COLUMN STRUM (LABOURA) (DDOOM |
| PA6/CLK2/TOUT1/BRGCLK1   | PA7/CLK1/TIN1/L1RCLKA/BRG01         |
| PA4/CLK4/TOUT2   |                                     |
| - Securios Antinomo | FAJ/CERJ/ INJ/DGROUIS               |
| PA2/CLK6/TOUT3/L1RCLK/BRGCLK2  | I F A I / CLN / / IIIAT / DONOT_    |
| PAO/CLK8/TOUT4/L1TCLKB   |                                     |

FIG. 51Y<sub>+</sub>





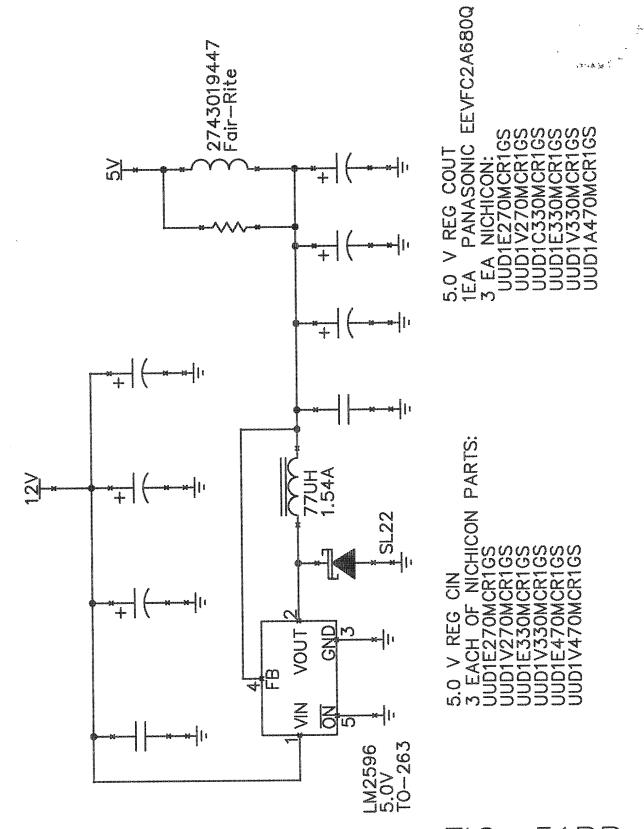


FIG. 51BB